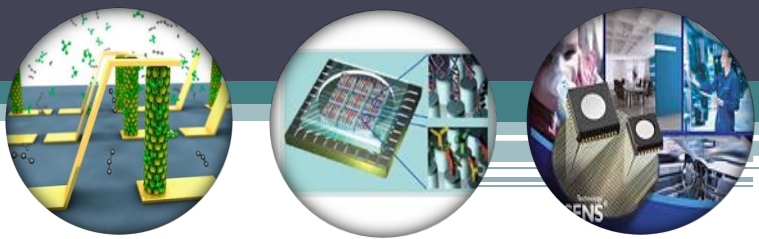




# Spectroscopic Interpretation of Transparent Oxide Films for Various Applications



**Kwun-Bum Chung**

Division of Physics and Semiconductor Science, Dongguk University  
 E-mail : [kbchung@dongguk.edu](mailto:kbchung@dongguk.edu)



# Introduction of CV

## Career

BS, Physics, Korea University (1996~2000)  
MS/PhD, Physics, Yonsei University (2000~2006)  
Post-Doc., KRISS (2006~2007)  
Post-Doc., NC state University (2007~2009)  
Assistant Prof., Physics, Dankook University (2009~2014)  
Associate Prof., Physics, Dongguk University (2014~)

## Research Interests

### Insulating oxides

- “Suppression of defect states in HfSiON gate dielectric film on n-type Ge(100) substrates” , Applied Physics Letters 93, 182903 (2008)
- “Effects of surface chemical structure on the mechanical properties of Si<sub>1-x</sub>Gex nanowires” , Nano Letter 12, 1118 (2013) 외 50 여편

### TCO & TOS

- “Enhancement of device performance and stability with homojunction structured tungsten doped InZnO thin film transistor” , Scientific Reports 7, 11634 (2017)
- “Unraveling the Issue of Ag Migration in Printable Source/Drain Electrodes Compatible to Versatile Solution-Processed Oxide Semiconductors for Printed Thin-Film Transistor Applications” , ACS Applied Materials & Interfaces 9, 14058 (2017)
- “Activation of sputter-processed indium-gallium-zinc oxide films by simultaneous ultraviolet and thermal treatments” , Scientific Reports 6, 21869 (2016) 외 80 여편

### Application & Analysis

- “A transparent solar cell based on a mechanically exfoliated GaTe and InGaZnO p-n heterojunction” , Journal of Materials Chemistry C 5, 4327 (2017)
- “Layer-by-Layer Assembled Graphene Multilayers on Multidimensional Surfaces for Highly Durable, Scalable, and Wearable Triboelectric Nanogenerators” , Journal of Materials Chemistry A 6, 3108 (2018)
- “Electron blocking layer-based interfacial design for highly-enhanced triboelectric nanogenerators” , Nano Energy 50, 9 (2018)
- “Area-selective Atomic Layer Deposition using Si Precursors as Inhibitors” , Chemistry of Materials 30, 7603 (2018) 외 40 여편

# Research topics

*Physical  
Properties*

*Electrical  
Properties*

*Optical  
Properties*

*Device  
Properties*

**Transformation, stability, transport mechanism  
Operation mechanism, reliability, etc**

***Why?***

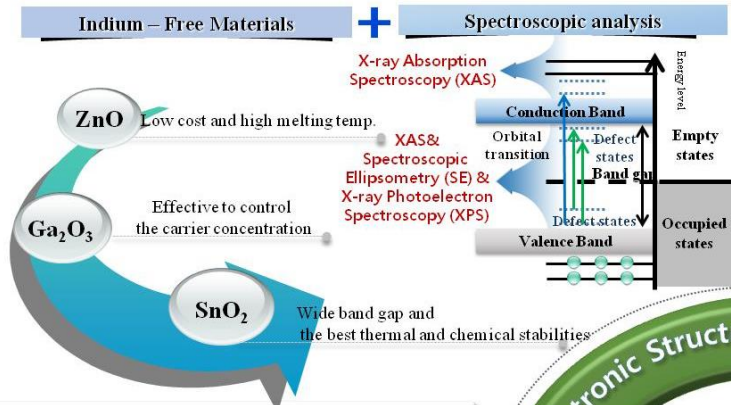
**Electronic structure  
Atomic configuration  
Defect states**

**Understanding of physics & Control of  
material properties**



# Research topics

## Electronic Structure of Transparent Oxide Films



Semiconductor properties of GZTO can be explained using spectroscopic analysis for each binary oxide in GZTO films

## Insulating Oxide for high mobility device and power device

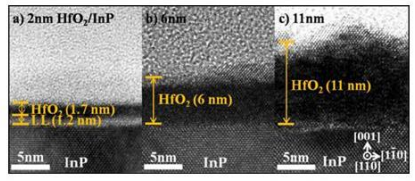


FIG. 1. Color online HR-TEM images of HfO<sub>2</sub> films grown on BOE treated InP with film thicknesses of a 2 nm b 6 nm, and c 11 nm, respectively.

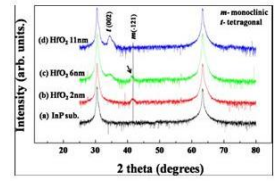


FIG. 2. Color online XRD data for HfO<sub>2</sub> films as a function of film thickness.

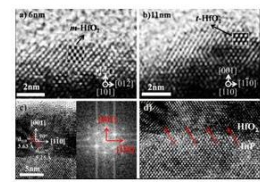


FIG. 3. The monoclinic structure is transformed into tetragonal structure, when the film thickness increase from 6 to 11 nm. The simulated image is in good agreement with the crystalline HRTEM images.

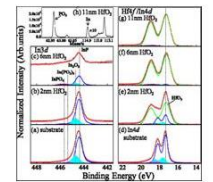
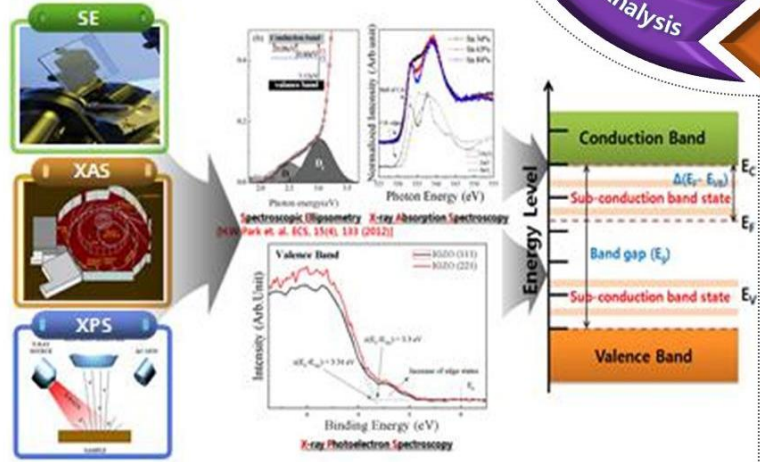


FIG. 4. Color online HR-XPS core-level spectra of In 3d left side and Hf 4f / In 4d right side in HfO<sub>2</sub> / InP.



## Spectroscopic Analysis of Multi-Functional Materials



## High-k dielectrics

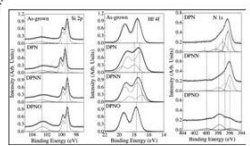


FIG. 1. XPS spectra a Si 2p, b Hf 4f, and c N 1s, as a function of various annealing conditions.

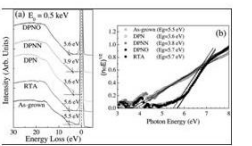


FIG. 2. A  $E_0=0.5$  keV. Band gap using b spectroscopic ellipsometry is well consistent with the REELS data.

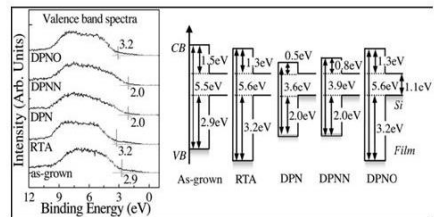


FIG. 3. Valence band spectra for the HfO<sub>2</sub> films left side and a band alignment diagram with Si right side as a function of various postannealing treatment.

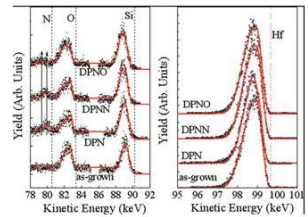


FIG. 4. Color online MEIS spectra for a change in concentration in the depth direction as a function of postannealing treatment.



## Introduction

## Electronic structure

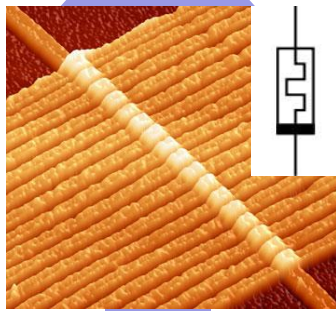
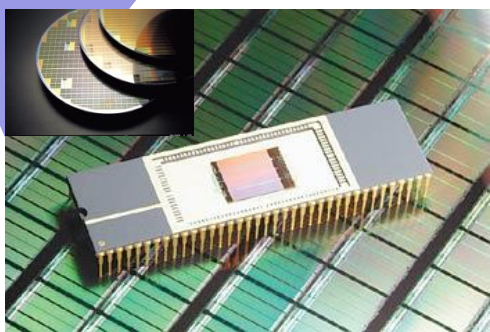
## Experimental results

- Transparent Conducting Oxides (TCO)
- Transparent Oxide Semiconductors (TOS)
- Transparent Oxide Thin Film Transistor (TTFT)

## Applications

# Diverse Applications of Oxide Films

**Memristor**



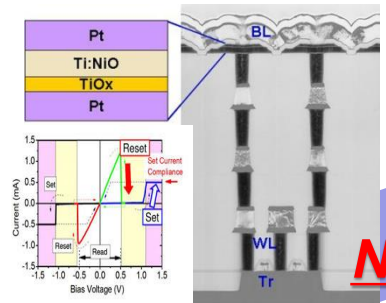
**Coating**



**CMOS, Memory**

**TCO, OTFT**

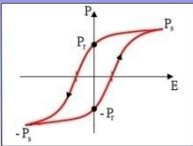
**Oxides Films**



**Non-volatile**

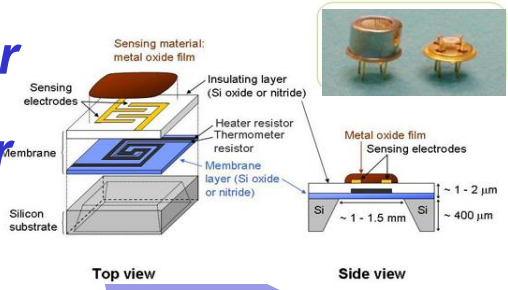


**memory**



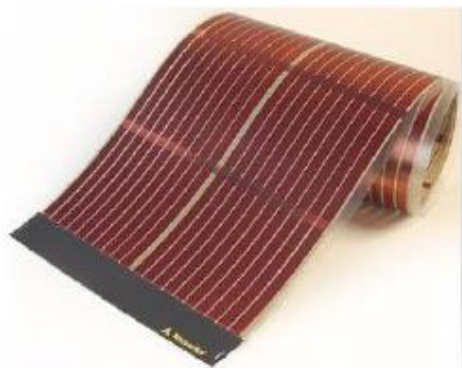
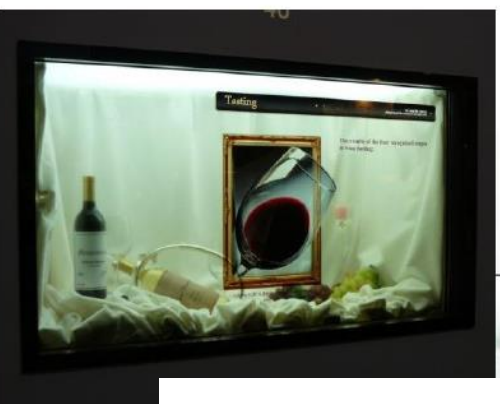
**Gas sensor**

**Bio sensor**



**Solar energy application**

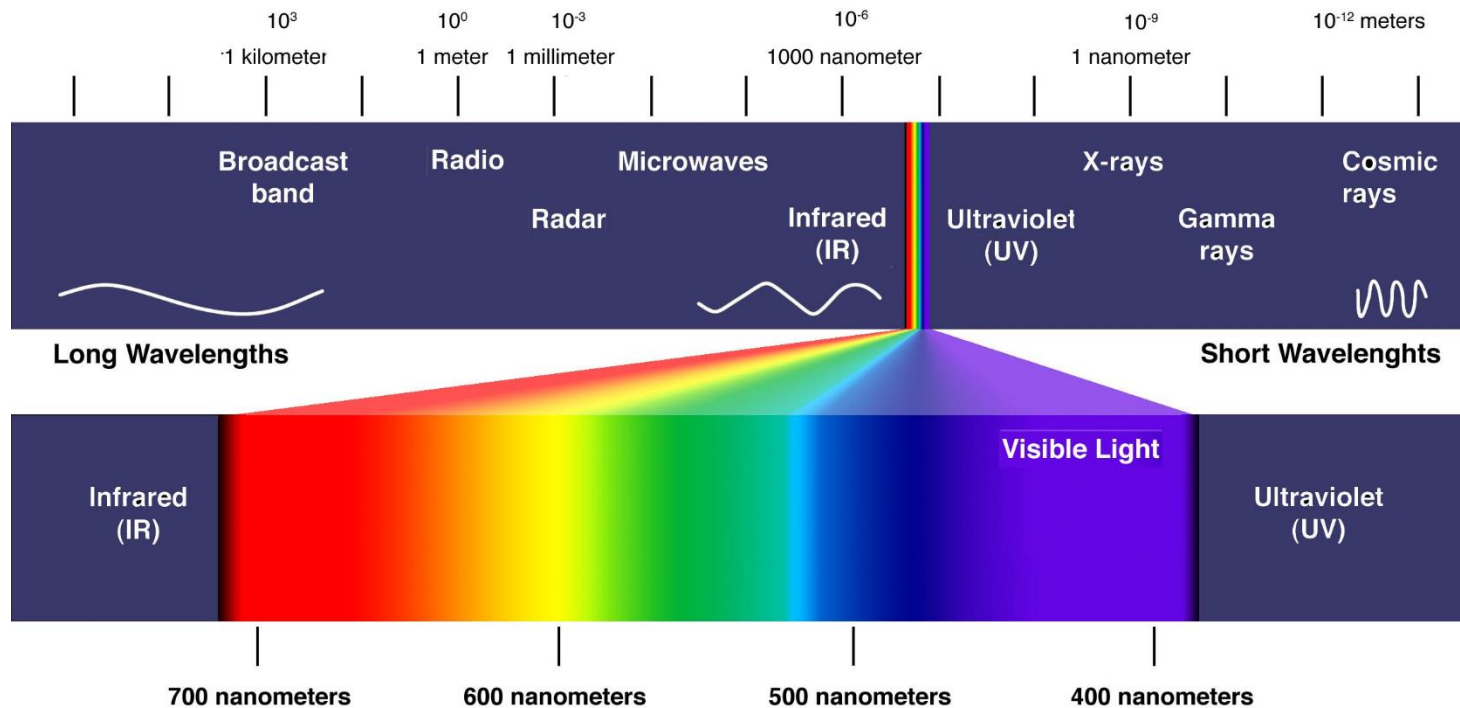
# Diverse Applications of Transparent Oxide Films



OPVs



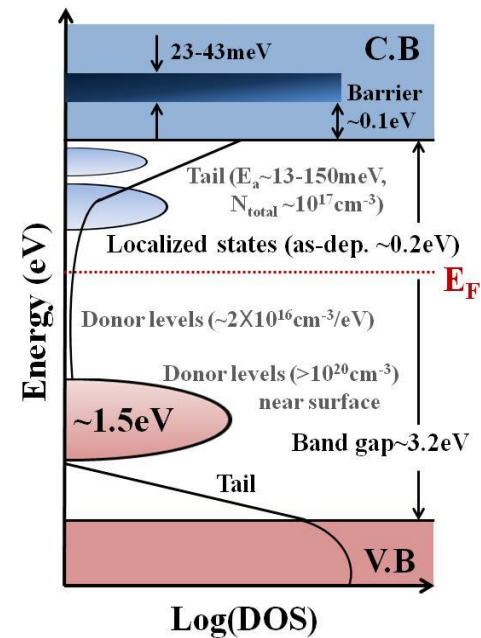
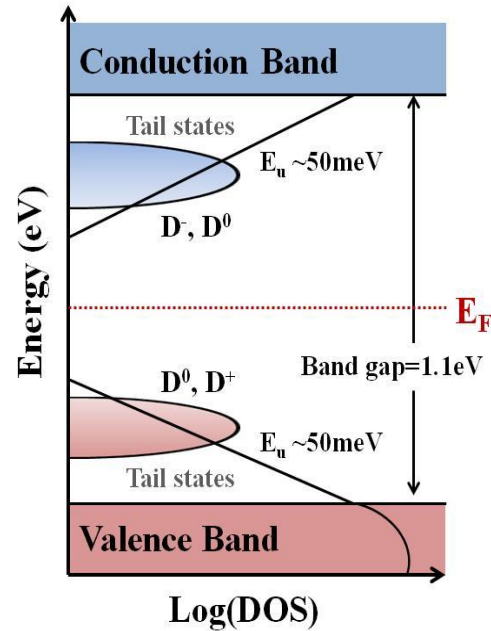
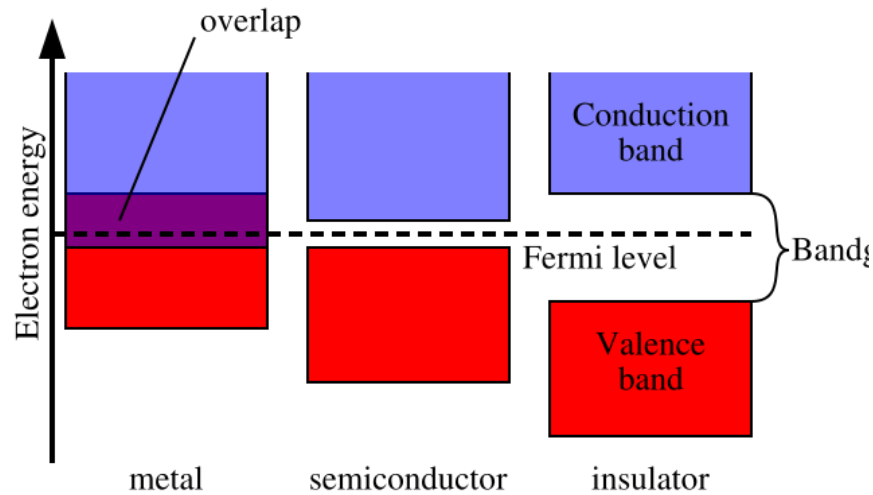
# Transparency?



## Transparency?

- Reflection, Absorption, Transmission
- Transmission of incident light  $\uparrow$  (Visible light, 300nm ~800nm)
- Absorption of incident light  $\downarrow$  (Bandgap  $< 3.5\text{eV}$ )

# Semiconducting? Conducting?



## - Semiconducting

→ Region between conductor and insulator, Change of conductivity depending on conditions, Bandgap

## - Conducting

→ High conductivity, Free electrons, No bandgap

# Transparent Conducting Oxides (TCO)

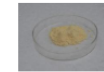
**High transparency**  
**High mobility & High concentration**  
**(High conductivity)**  
**Low cost & Low Temp. & Large area**

- Resistivity <math> < 10^{-3} \Omega \cdot \text{cm}</math>
- Sheet resistance <math> < 10^3 \Omega / \text{sq}</math>
- Transmittance > 80% (380nm~780nm visible range)
- Key components for FPD, Photovoltaic, Touch panels and Electronics
- Cost-efficient materials

1. CdO (1907)

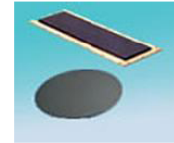


2.  $\text{In}_2\text{O}_3$  (1954, G. Rupprecht)



3. Impurity-doped binary compound

$\text{SnO}_2$  doped  $\text{In}_2\text{O}_3$  : ITO (1968, van boort and groth)  
 F-doped  $\text{SnO}_2$  :FTO  
 $\text{Sb}_2\text{O}_5$  doped  $\text{SnO}_2$ :ATO

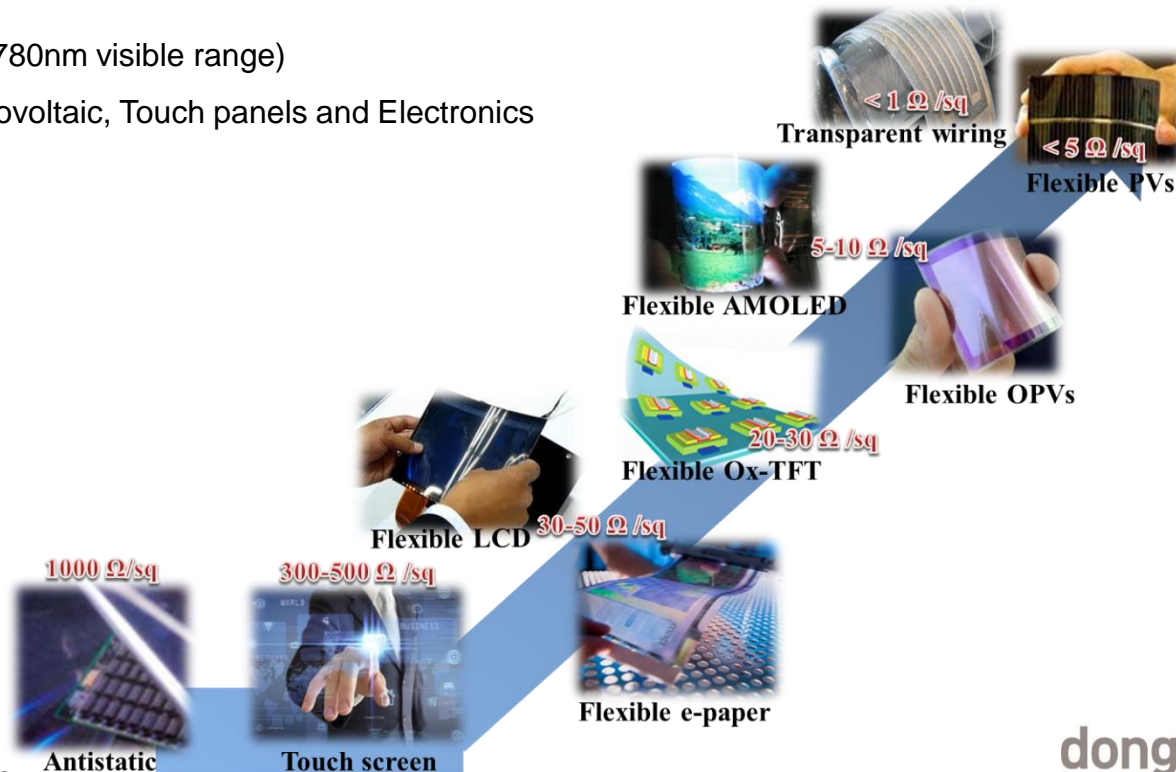


4. 1990~2000: TCO films for specialized applications

Multicomponents oxide: combination of binary compound such as Al-ZnO, Ga-ZnO, B-ZnO, CdO,  $\text{Ga}_3\text{O}_2$ ,  $\text{SnO}_2$ ,  $\text{In}_2\text{O}_3$ ,  $\text{Zn}_2\text{SnO}_4$ ,  $\text{MgIn}_2\text{O}_4$ ,  $\text{CdSb}_2\text{O}_6$ :Y, ZnSnO,  $\text{GaInO}_3$ ,  $\text{Zn}_2\text{In}_2\text{O}_5$ ,  $\text{In}_4\text{Sn}_3\text{O}_{12}$

5. 2000~ : New transparent conducting materials

PEDOT:PSS, CNT based electrode, Graphene, Grid, Indium free TCO





## AMOLED



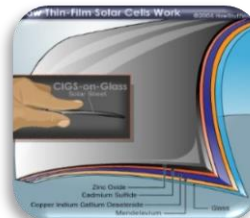
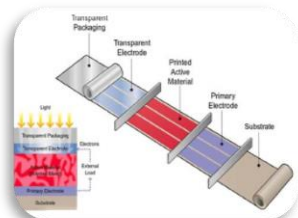
- Resistivity:  $10^{-4} \Omega\text{-cm}$
- Sheet resistance:  $1 \Omega/\text{sq}$
- Optical Transmittance:  $> 85\%$
- Work function:  $> 4.8 \text{ eV}$
- RMS roughness:  $2\text{nm}$
- Chemical stability
- Stability against humidity:  $85^\circ\text{C}, 85\%$
- Good wet etching
- Low cost element

## Touch Panel



- High Transparency:  $>85\%$
- Sheet resistance:  $400\sim500 \Omega/\text{sq}$
- Uniformity
- Reliability against temperature and moisture
- Haze properties
- Chemical and mechanical stability
- Continuous process

## Solar Cell



$$\eta_{EQE} = \eta_A \eta_{IQE} = \eta_A \eta_{ED} \eta_{CT} \eta_{cc}$$

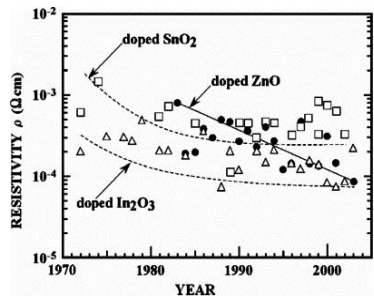
$\eta_A$  : Photon absorption

$\eta_{cc}$  : Collection of Carrier

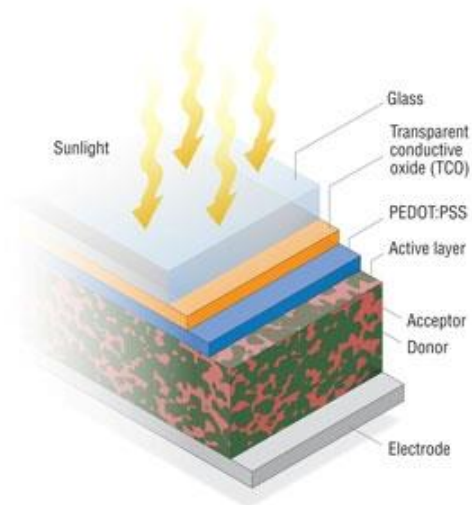
- High Transparency:  $>85\%$
- Very low resistivity:  $\sim 10^{-4} \Omega\text{-cm}$
- Smooth morphology
- Low cost (indium free-TCO)
- Superior flexibility
- Low temperature process
- Chemical stability against acid solution
- R2R based Large area coating technique
- Reliability

# Candidates of TCO

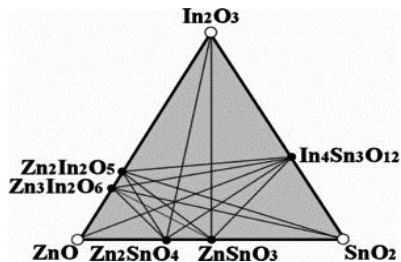
## Indium free



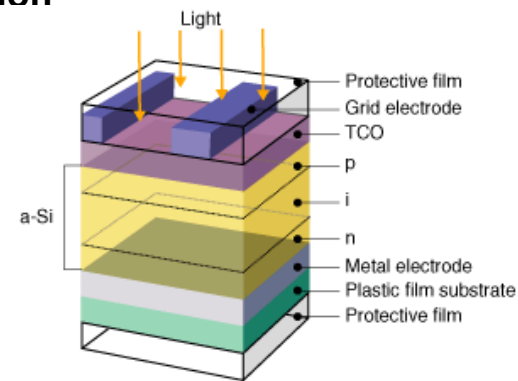
- ZnO, TiO, SnO<sub>2</sub> based TCO
- Al-ZnO, Ga-ZnO, B-ZnO, F-SnO<sub>2</sub>, Zn-SnO<sub>2</sub>, Sb-SnO<sub>2</sub>, Nb-TiO<sub>2</sub>(NTO), Ta-TiO<sub>2</sub>, Nb-Ta<sub>2</sub>O<sub>5</sub>



## Indium reducing

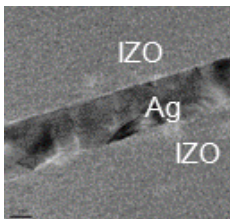


- Multicomponents TCO to reduce in composition
- InZnSnO (IZTO), InAlZnO, InAlSnZnO, InGaSnO, InGaZnO, TiInSnO (TITO)



Amorton Film Configuration

## Multi-layered

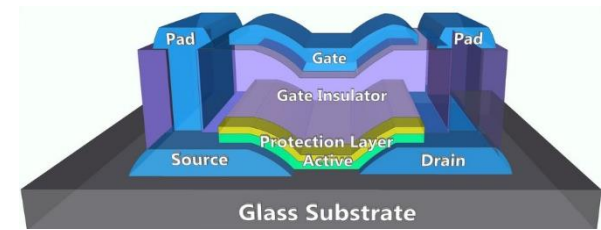


- Oxide/Metal/Oxide
- ITO/Ag/ITO, IZO/Ag/IZO, IZTO/Ag/IZTO, GZO/Ag/GZO, AZO/Ag/AZO, NTO/Ag/NTO, BZO/Ag/BZO, TiO<sub>2</sub>/Ag/TiO<sub>2</sub>

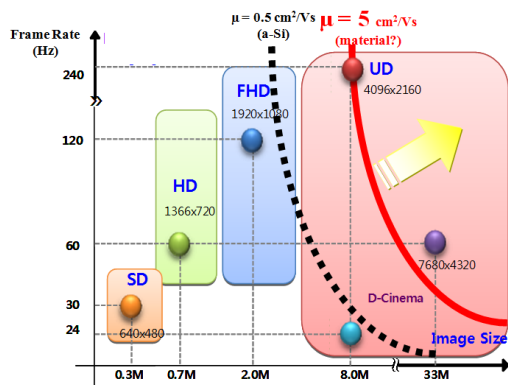
출처 : 2011 Displaybank 교육자료

# Transparent Oxide Semiconductor

High transparency  
 High mobility & High concentration  
 High quality & controllability  
 (Density of State)



Channel layer for TFTs



Large Area



High Resolution



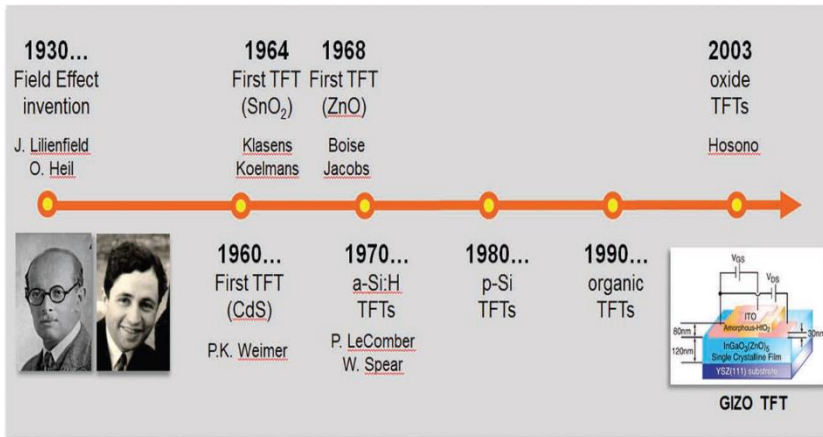
Fast Frame Rate



	a-Si TFT	poly-Si TFT	Oxide TFT
Semiconductor	Amorphous Si	Polycrystalline Si	Amorphous oxide
Transparency	No	No	Yes
TFT uniformity	Good	Poor	Good
Channel mobility	1 cm <sup>2</sup> /Vs	~100 cm <sup>2</sup> /Vs	10~100 cm <sup>2</sup> /Vs
Pixel circuit	Simple (1T+1C)	Complex (5T+2C)	Simple (2T+1C)
Cost/Yield	Low/High	High/Low	Low/High
Process Temp.	~250°C	>250°C	RT~250°C
$\Delta V_{th}$ (@I <sub>DS</sub> =3μA)	>30V	<0.5V	~1.7V



# History

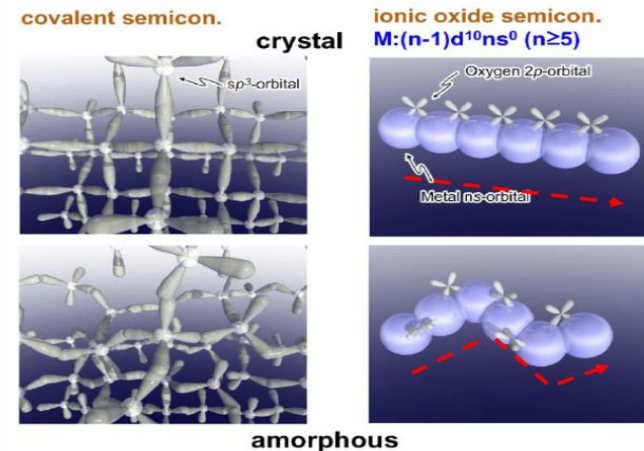


## • The Advantages

- **High Mobility** (compared to a-Si)
- **Room Temperature Deposition (Sputtering)**
- **Amorphous (no G.B. : Large Area Uniformity)**

## • Conduction Mechanism

- **No Directionality**
- **Oxygen Deficiency – Carrier Generation**
- **Overlap of Vacant s Orbitals**
- **Ga-In-Zn-O (GIZO) is Best Known**

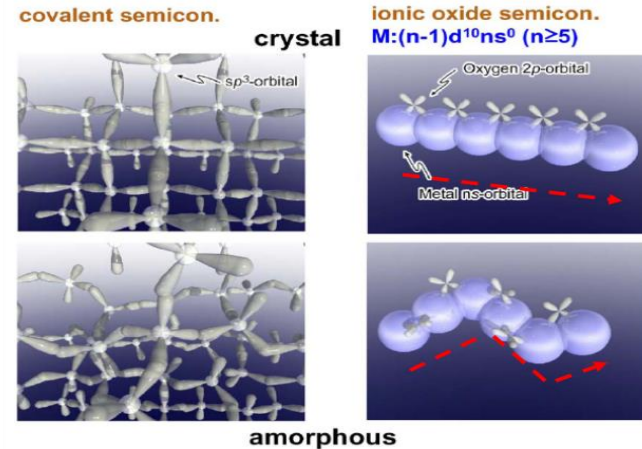


H. Hosono, *J. Non-Cryst. Solids* 352 (2006)

# Oxygen vacancy

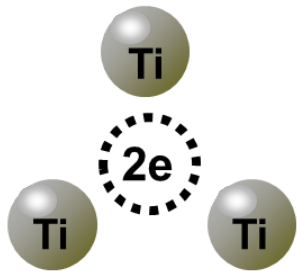
## Conduction Mechanism

- No Directionality
- Oxygen Deficiency – Carrier Generation
- Overlap of Vacant s Orbitals
- Ga-In-Zn-O (GIZO) is Best Known

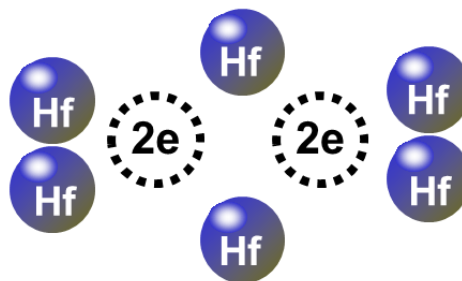


H. Hosono, *J. Non-Cryst. Solids* 352 (2006)

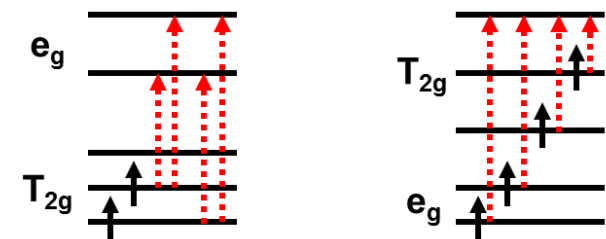
### TiO<sub>2</sub> monovacancy



### HfO<sub>2</sub> divacancy



### conduction band edge



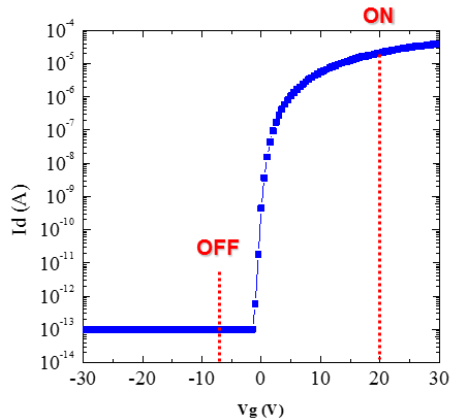
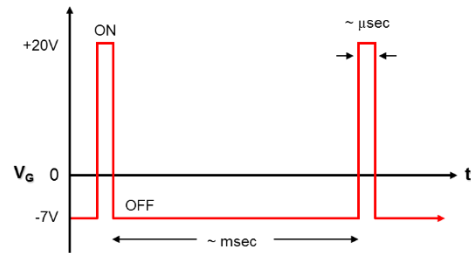
### valence band edge

## Bias Instability

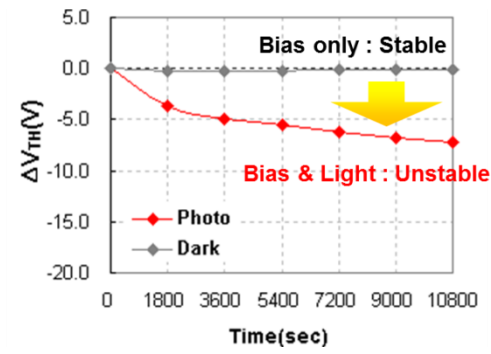
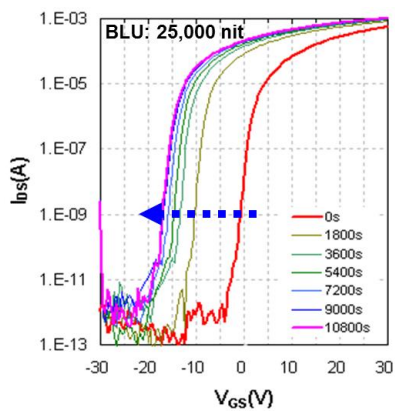
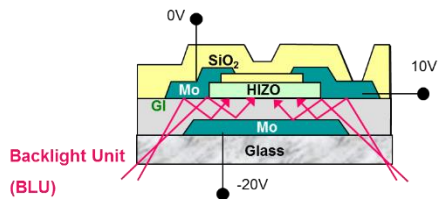
Frame Scan



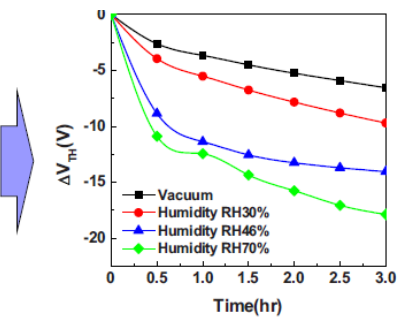
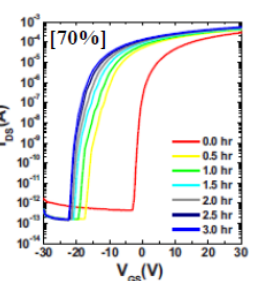
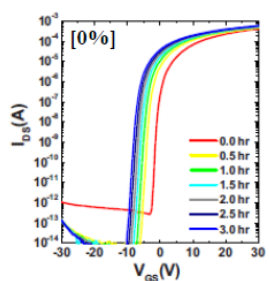
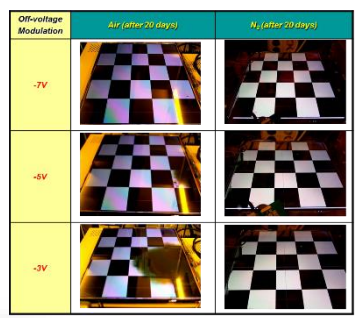
Gate Signal in Each Pixel TFT



## Illumination Instability



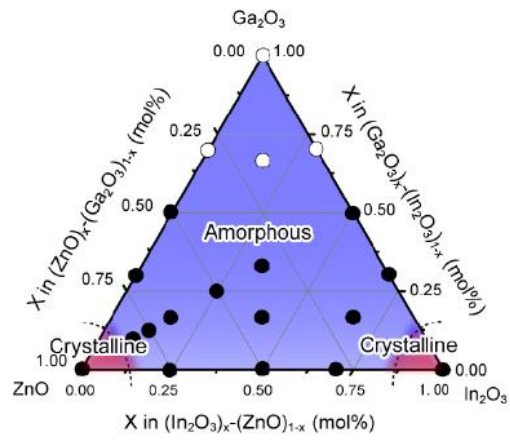
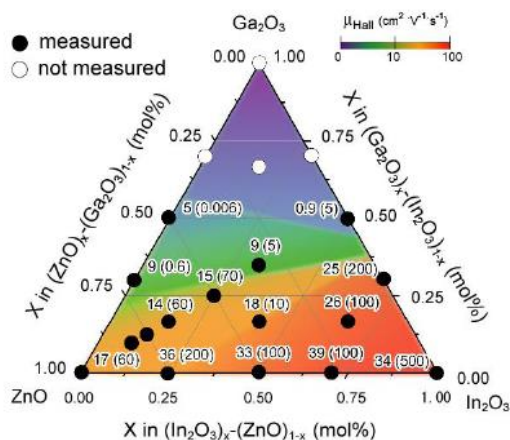
## Environment Instability



# Candidates of TOS

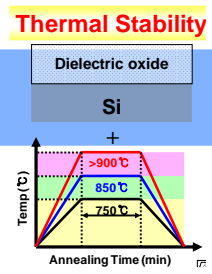
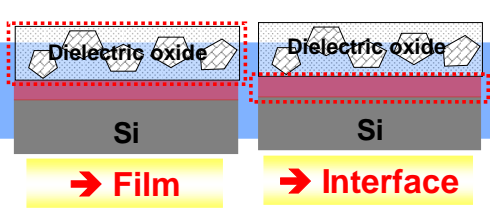
Material	Dopant or Compound
SnO <sub>2</sub>	Sb, F, As, Nb, Ta
In <sub>2</sub> O <sub>3</sub>	Sn, Ge, Mo, F, Ti, Zr, Hf, Nb, Ta, W, Te
ZnO	Al, Ga, B, In, Y, Sc, F, V, Si, Ge, Ti, Zr, Hf
CdO	In, Sn
ZnO-SnO <sub>2</sub>	Zn <sub>2</sub> SnO <sub>4</sub> , ZnSnO <sub>3</sub>
ZnO-In <sub>2</sub> O <sub>3</sub>	Zn <sub>2</sub> In <sub>2</sub> O <sub>5</sub> , Zn <sub>3</sub> In <sub>2</sub> O <sub>6</sub>
In <sub>2</sub> O <sub>3</sub> -SnO <sub>2</sub>	In <sub>4</sub> Sn <sub>3</sub> O <sub>12</sub>
CdO-SnO <sub>2</sub>	Cd <sub>2</sub> SnO <sub>4</sub> , CdSnO <sub>3</sub>
CdO-In <sub>2</sub> O <sub>3</sub>	CdIn <sub>2</sub> O <sub>4</sub>
MgIn <sub>2</sub> O <sub>4</sub>	
GaInO <sub>3</sub> , (Ga,In) <sub>2</sub> O <sub>3</sub>	Sn, Ge
CdSb <sub>2</sub> O <sub>6</sub>	Y
ZnO-In <sub>2</sub> O <sub>3</sub> -SnO <sub>2</sub>	Zn <sub>2</sub> In <sub>2</sub> O <sub>5</sub> -In <sub>4</sub> Sn <sub>3</sub> O <sub>12</sub>
CdO-In <sub>2</sub> O <sub>3</sub> -SnO <sub>2</sub>	CdIn <sub>2</sub> O <sub>4</sub> -Cd <sub>2</sub> SnO <sub>4</sub>
ZnO-CdO-In <sub>2</sub> O <sub>3</sub> -SnO <sub>2</sub>	

Channel Materials	Deposition Method	Mobility (cm <sup>2</sup> /V · s)	Subthreshold Swing (V/decade)	Process Temperature (°C)
<i>a</i> -ZnON	Reactive sputter	10	0.8	350
<i>p</i> -ZnON	ALD	6.7	0.67	150
MgZnO	MOCVD	40	0.25	450
<i>a</i> -InZnO	Sputter	20	1.2	R.T.
<i>a</i> -InZnO	Sputter	4.5	0.87	40
<i>a</i> -InZnO	Sputter	27 (19)	ND	600 (200)
<i>a</i> -InGaZnO	PLD	9	ND	R.T.
<i>a</i> -InGaZnO	Sputter	12	0.2	R.T.
<i>a</i> -InGaZnO	Sputter	35.9	0.59	350
ZrInZnO	Sputter	3.9	0.98	350
<i>a</i> -HfInZnO	Sputter	10	0.23	200
<i>a</i> -SnInZnO	Sputter	24.6	0.12	300
<i>a</i> -AlSnInZnO	Sputter	31.4	0.14	250
SiInZnO	Sputter	21.6	1.52	150
<i>a</i> -ZrSnO	Sputter	5-15 (20-50)	ND	300 (600)
<i>a</i> -ZrSnO	PLD	10	1.4	450
ZnSnO	Sputter	14	1.6	250
<i>a</i> -AlZrSnO	Sputter	10.1	0.6	180
GaZrSnO	Sputter	24.6	0.38	300
ZrZnSnO	Sputter	8.9	0.7	350

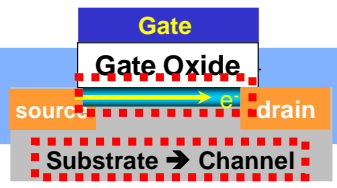
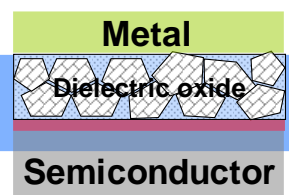




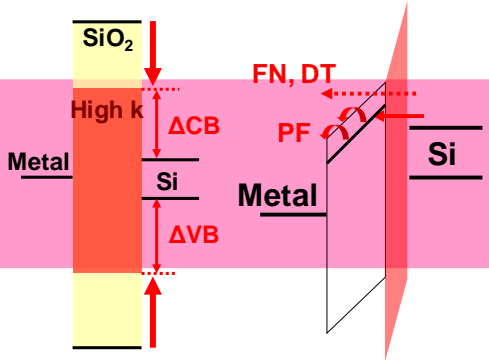
# Characterization Issues



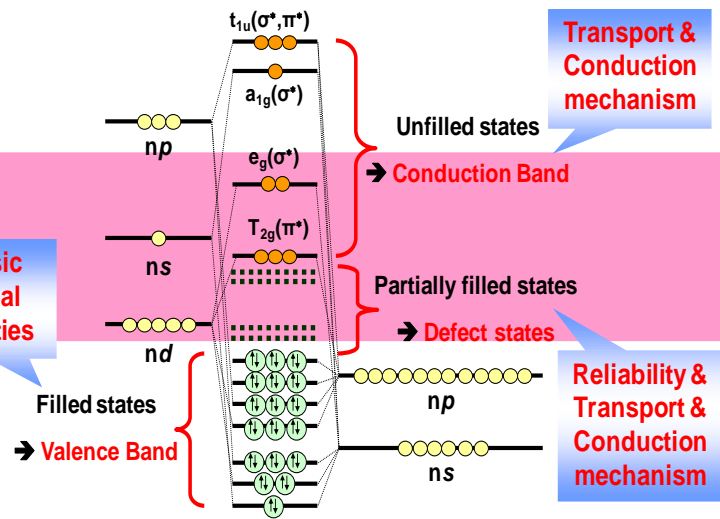
**Physical Properties**  
(cleaning, growth)



**Electrical Properties**  
(film, device)



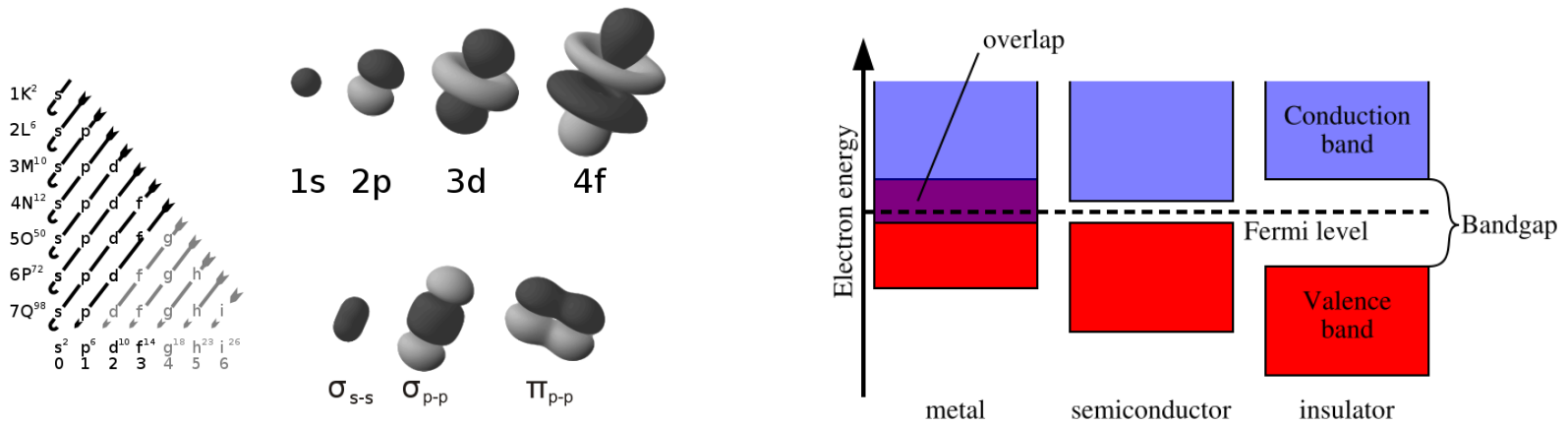
**Intrinsic Material Properties**



**Electronic Structure**  
(Band Structure, Atomic Configuration, Defects)

**→ Importance of understanding for electronic structure**

# Importance of electronic structure

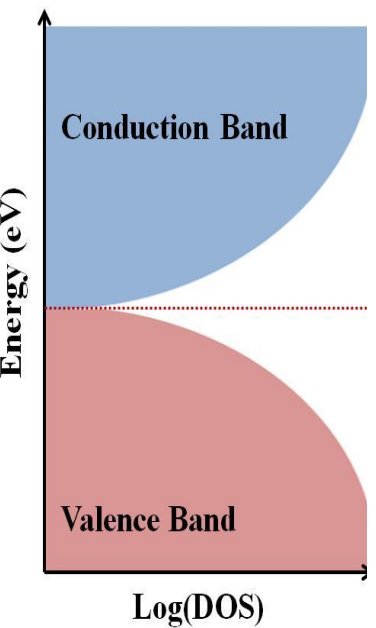


$1s^2 2s^2 2p^6 3s^2 3p^6 4s^2 3d^{10} 4p^6 5s^2 4d^{10} 5p^6 6s^2 4f^{14} 5d^{10} 6p^6 7s^2 5f^{14} 6d^{10} 7p^6$

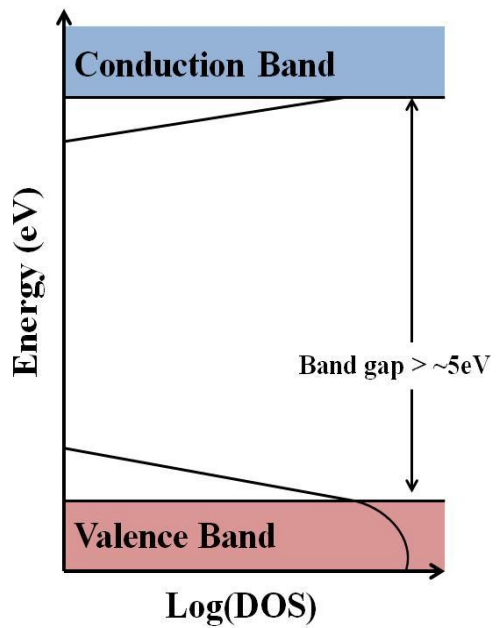
	Atom	Molecule	Insulator	Semiconductor	Doped semiconductor	Metal
Structure						
Dispersion relation						

Answers how the electrons behave in matter. → Intrinsic material property, conducting & transport mechanism, reliability

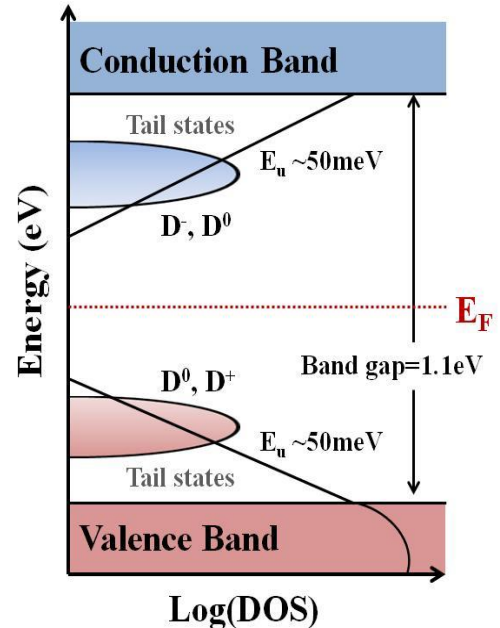
# Electronic Structures of Solid



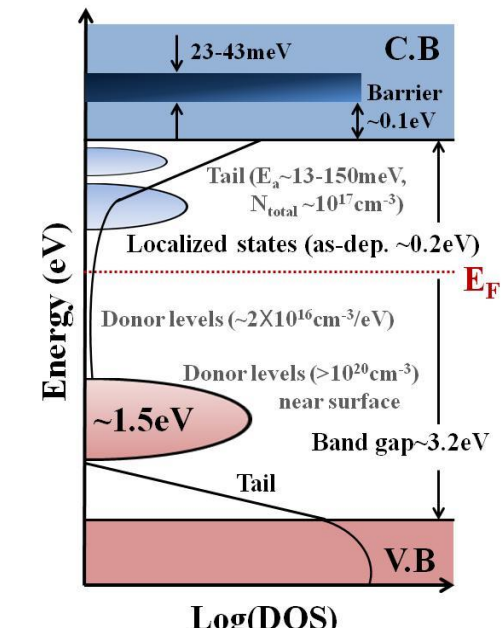
[Metal DOS model]



[Insulator DOS model]



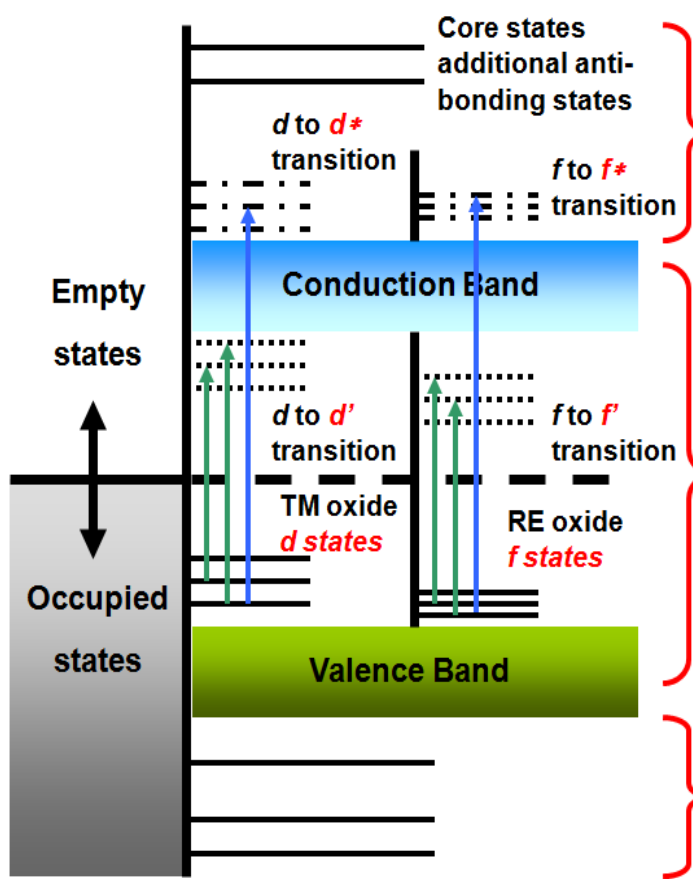
[a-silicon DOS model]



[a-IGZO DOS model]

**→ Importance of understanding for Electronic Structure!!**

# Analysis of Electronic Structure



**Virtual bound state regime**

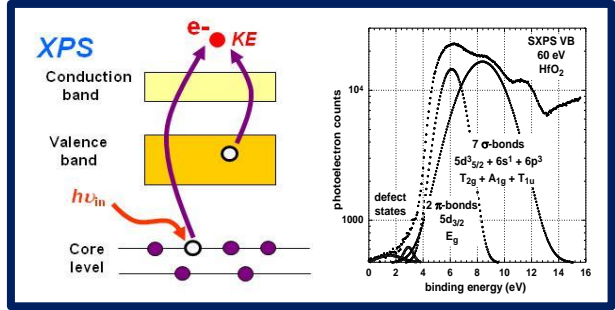
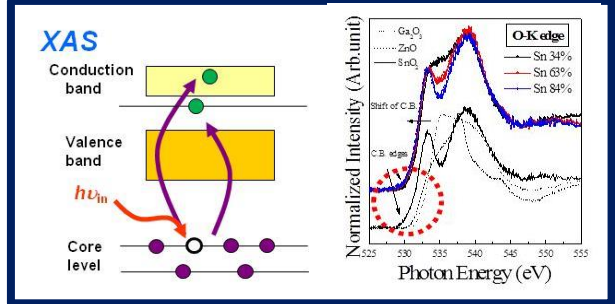
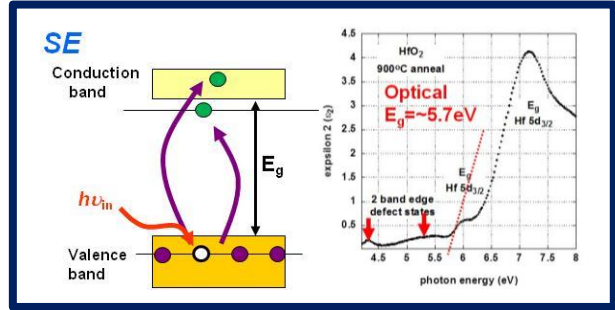
X-ray Absorption Spectroscopy (XAS)

**Pre-edge regime**

XAS & Spectroscopic Ellipsometry (SE) & X-ray Photoelectron Spectroscopy (XPS)

**Shallow and deep core regime**

XPS & X-ray Emission Spectroscopy (XES)



**Spectroscopic analysis by SE, XAS, XPS, XES in PAL, SSRL, PF**





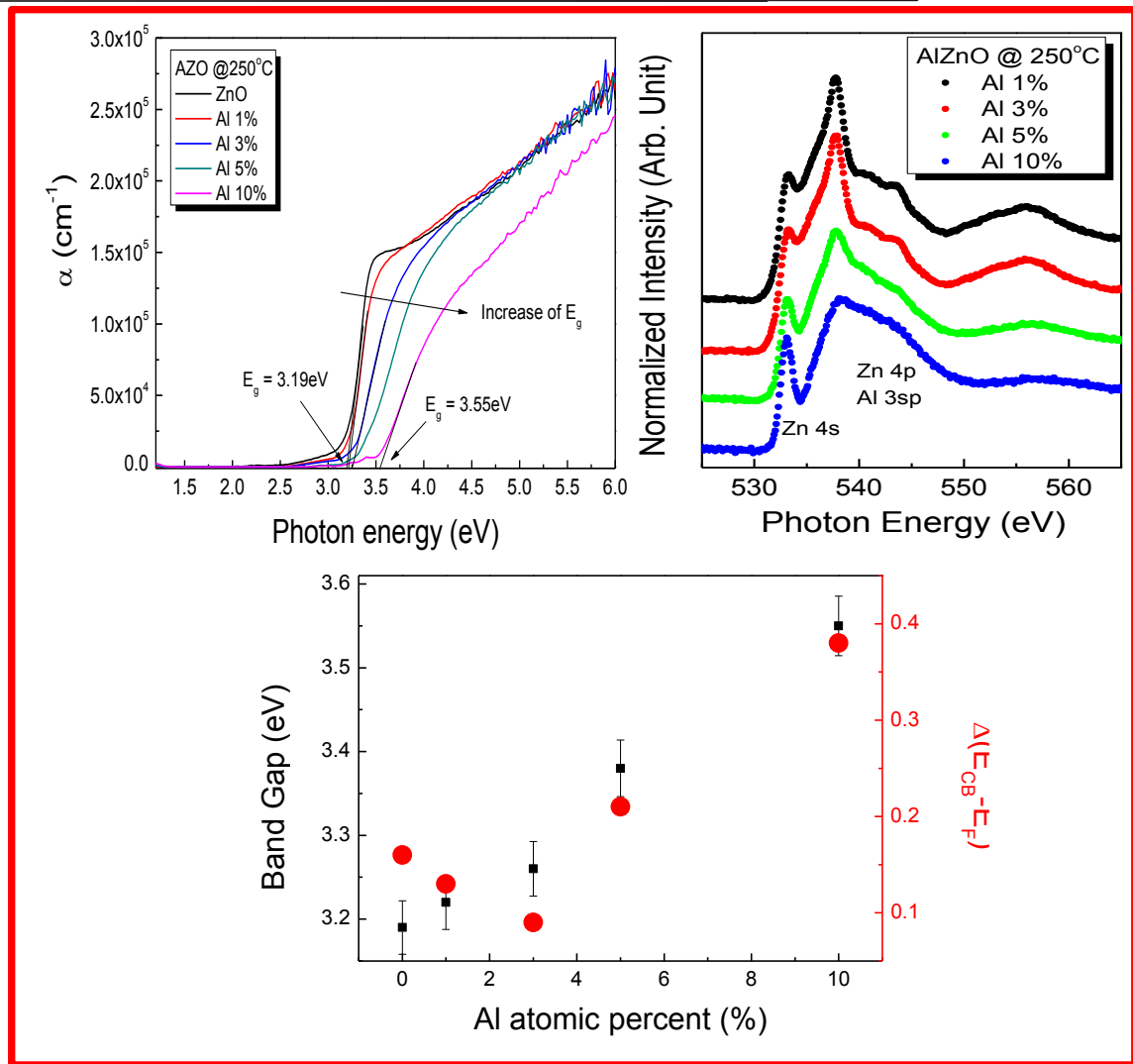
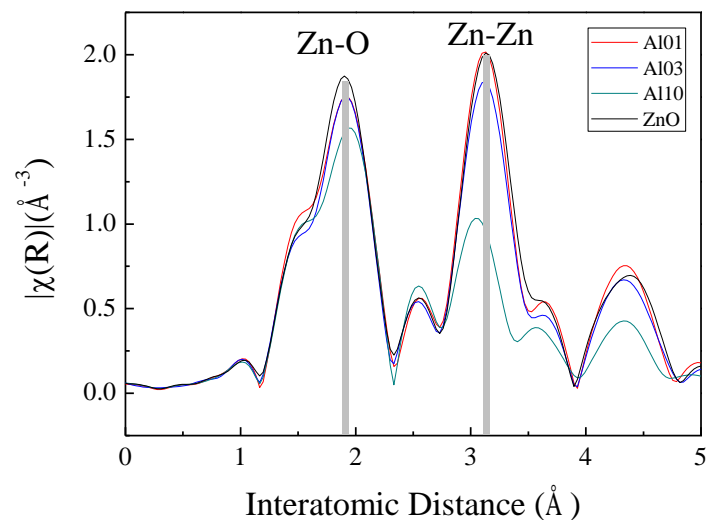
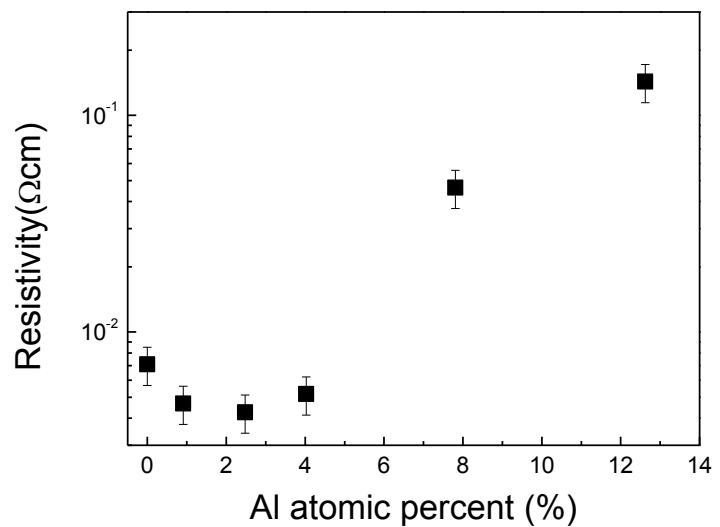
## Introduction

## Electronic structure

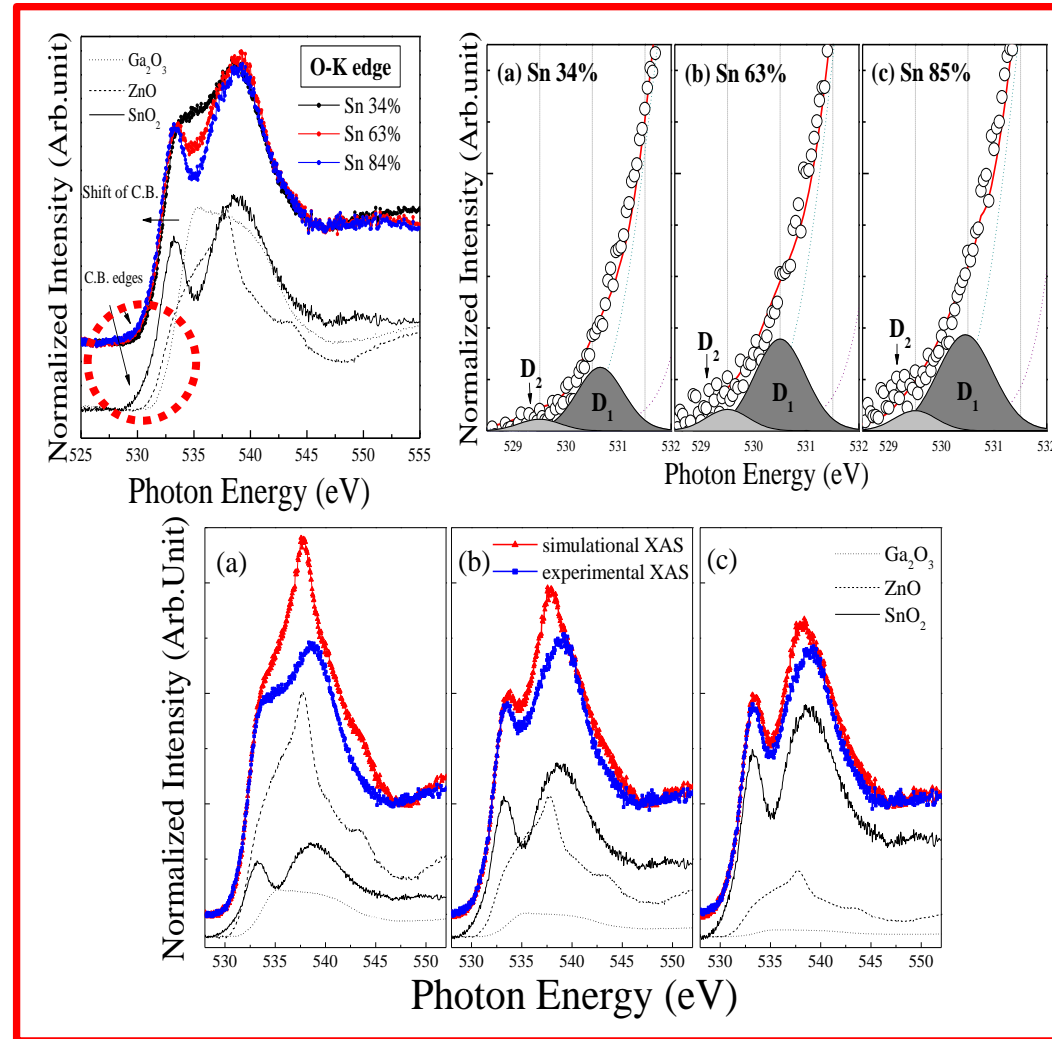
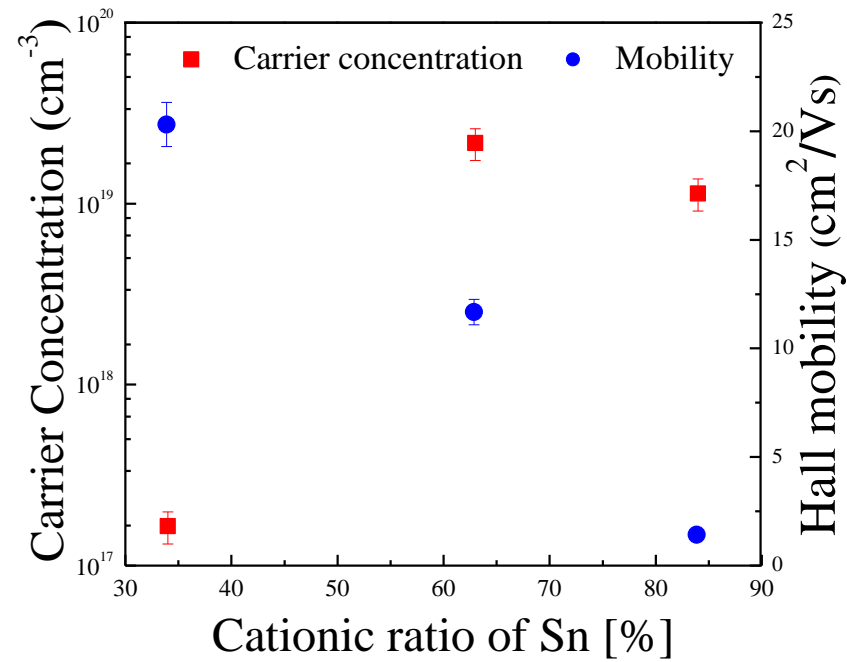
## Experimental results

- **Transparent Conducting Oxides (TCO)**
- **Transparent Oxide Semiconductors (TOS)**
- **Transparent Oxide Thin Film Transistor (TTFT)**

## Applications



**→ Relation between electrical property and electronic structure (conduction band & bandgap & band offset) Chung et. al. CI 41, 1641 (2015)**



**→ Relation between electrical property and electronic structure (band edge & electronic structural boundary)**

Chung et. al. APL 104, 182106 (2014)

## Introduction

## Electronic structure

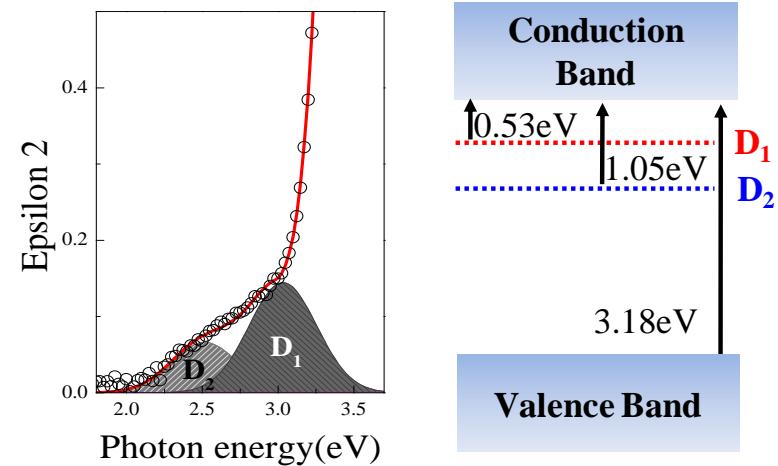
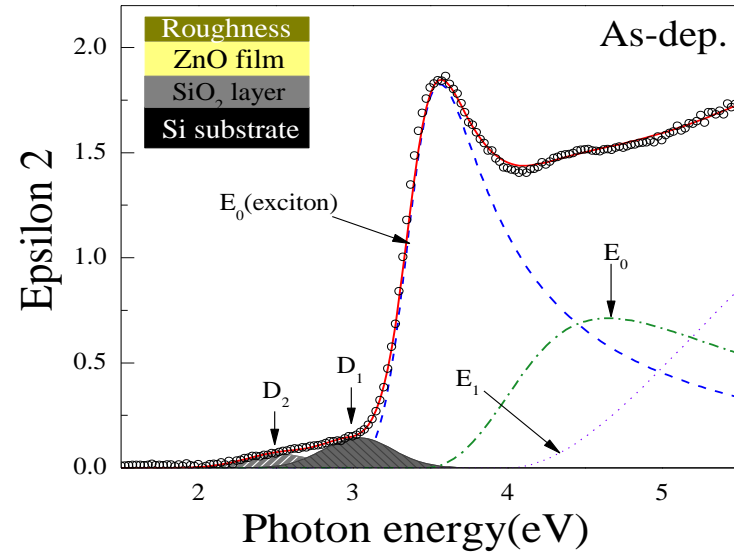
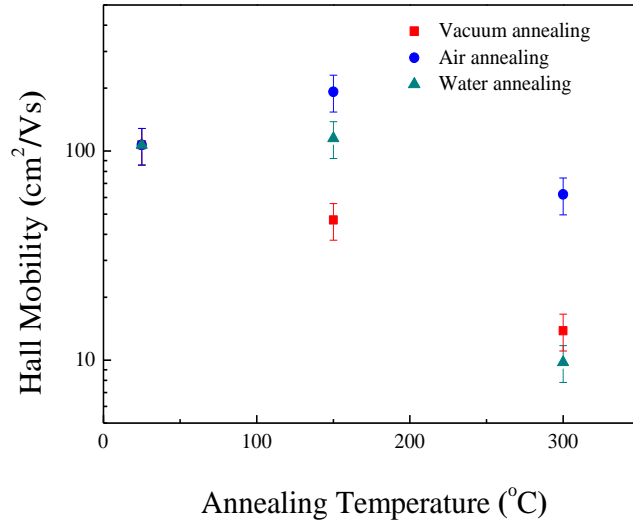
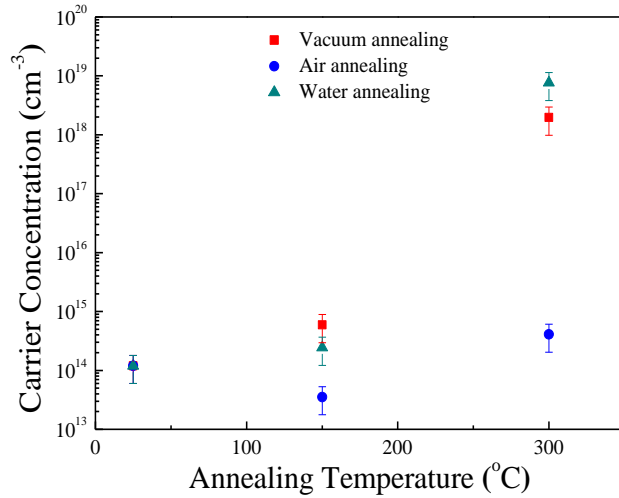
## Experimental results

- Transparent Conducting Oxides (TCO)
- Transparent Oxide Semiconductors (TOS)
- Transparent Oxide Thin Film Transistor (TTFT)

## Applications



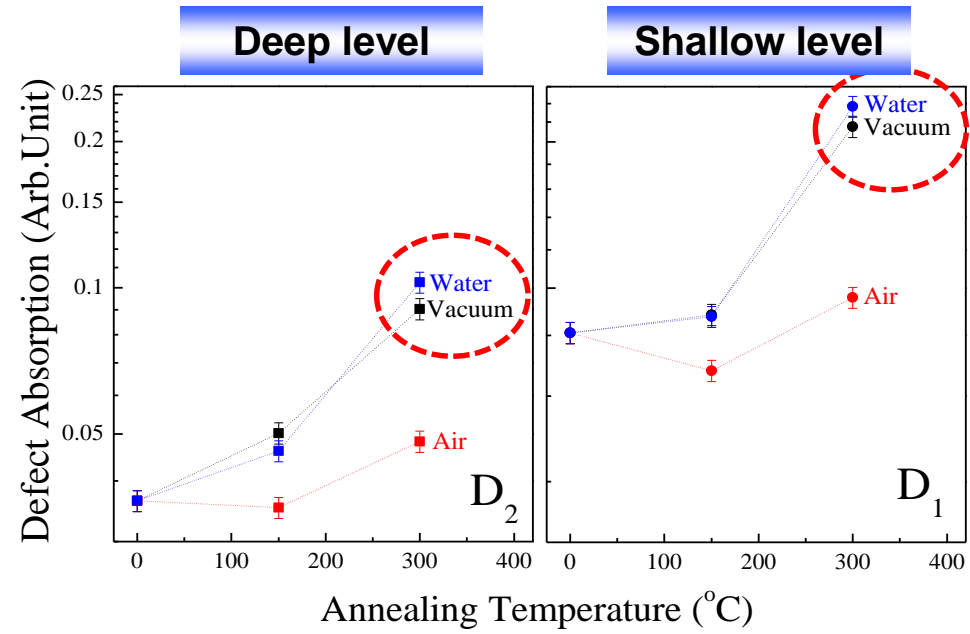
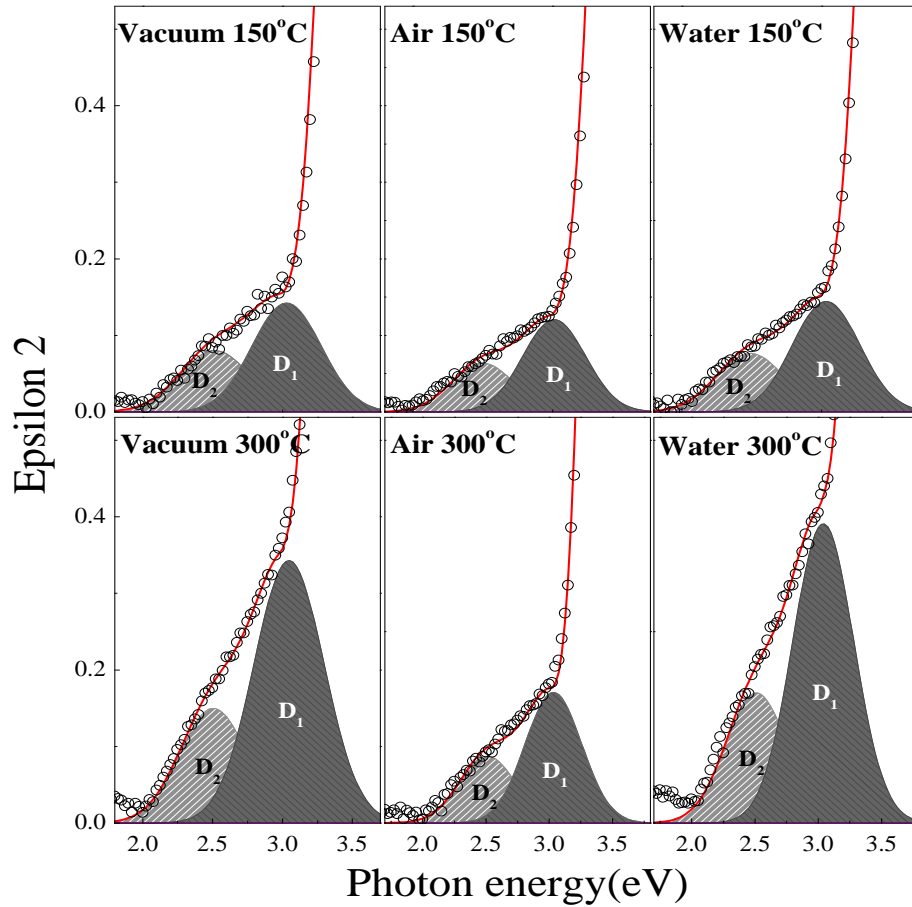
# ZnO by thermal treatments



**→ Changes of electrical properties by thermal treatments**

Chung et. al. ESL 15, H133 (2012)

# ZnO by thermal treatments

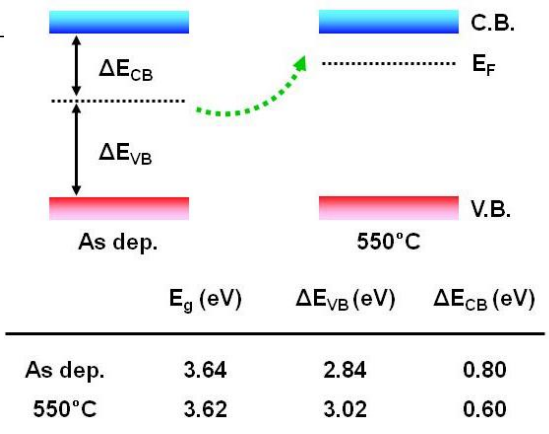
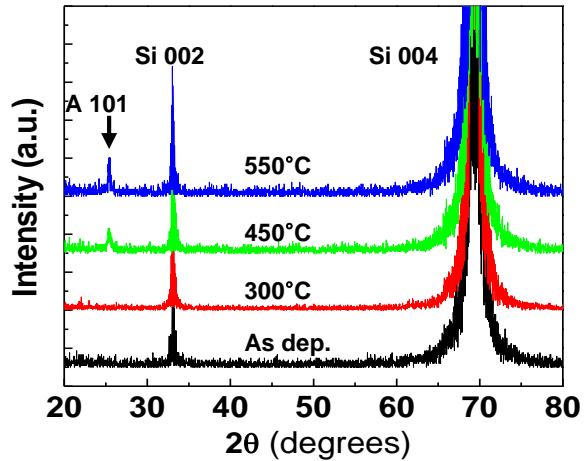
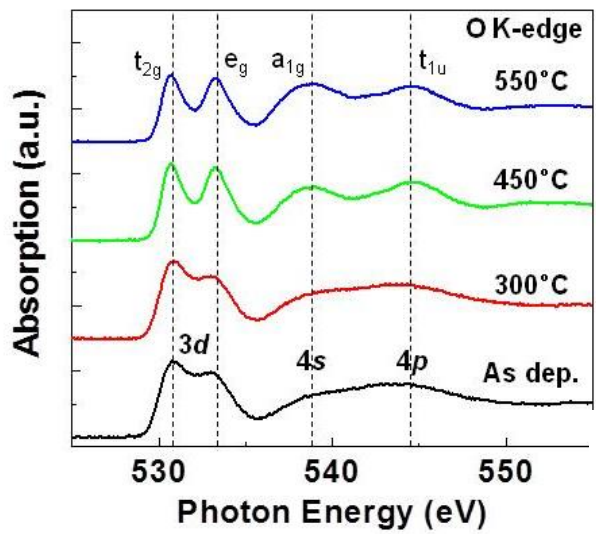
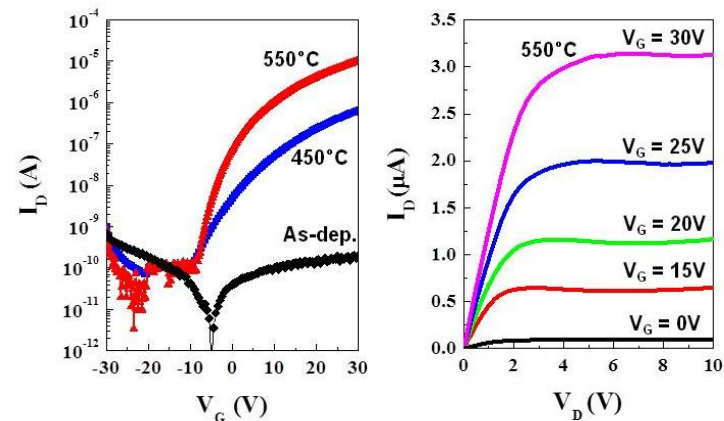


→ Deep level : mobility

→ Shallow level : carrier concentration

Chung et. al. ESL 15, H133 (2012)

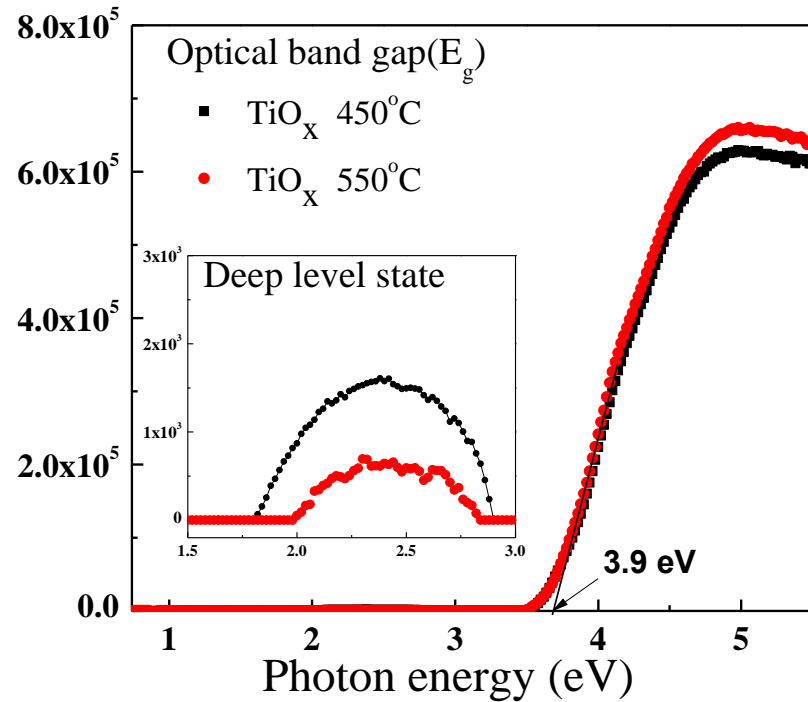
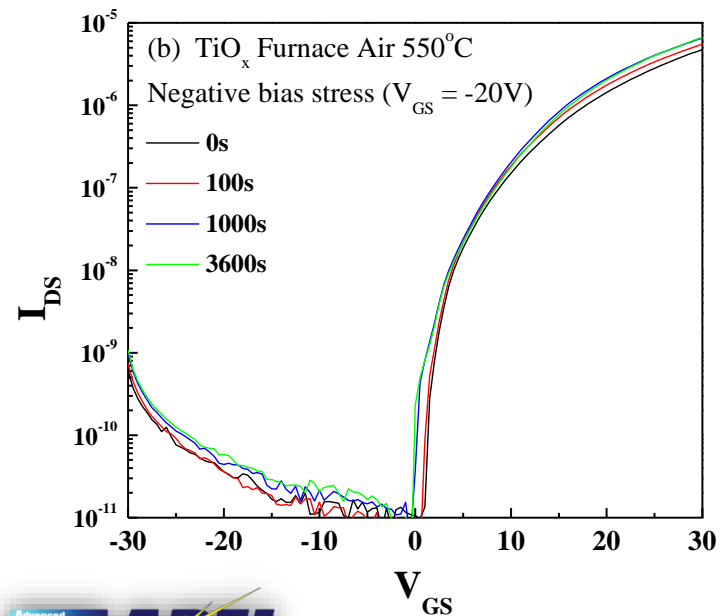
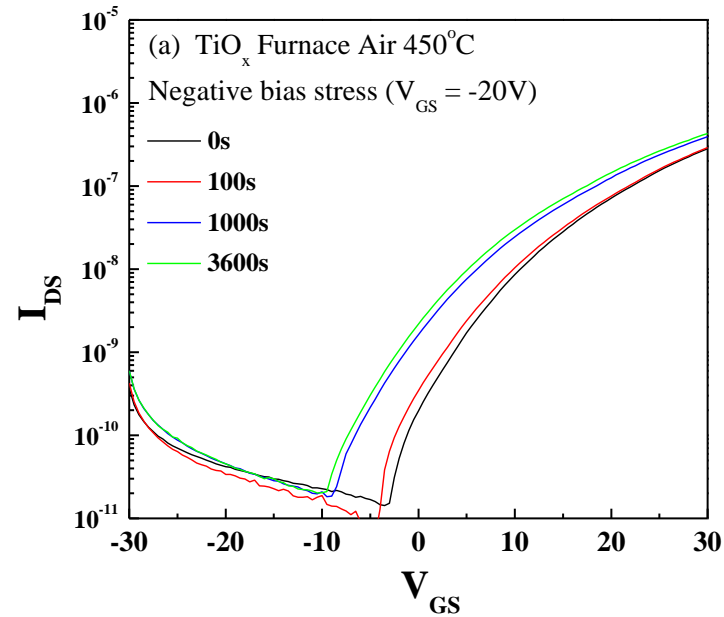
# TiO<sub>2-x</sub> TFT



- Device operation of TiO<sub>2-x</sub> annealed above 450°C
- Anatase structure
- Changes in molecular orbital ordering
- Decrease of conduction band offset

Chung et. al. APL 99, 142104 (2011)

# TiO<sub>2-x</sub> reliability

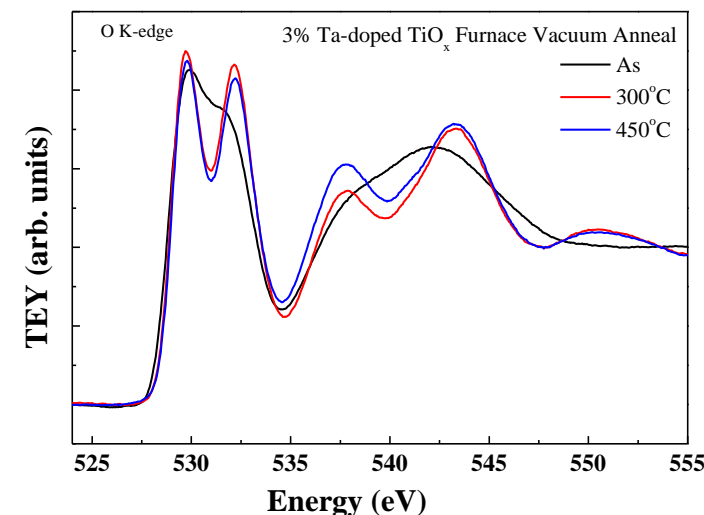
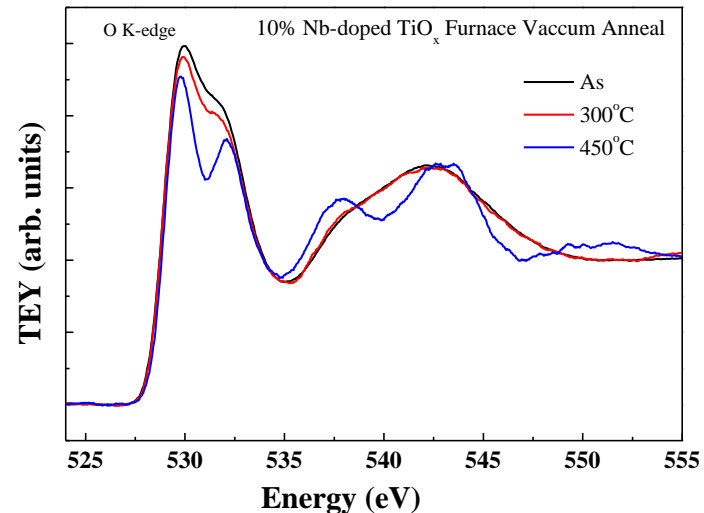
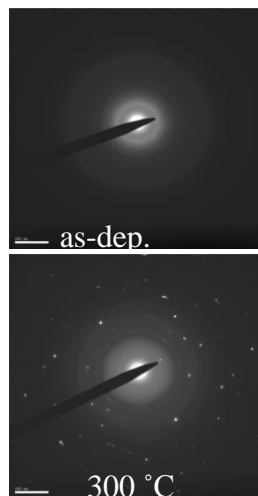
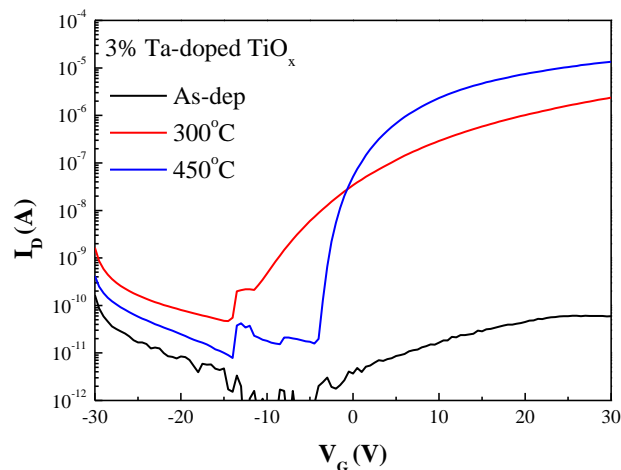
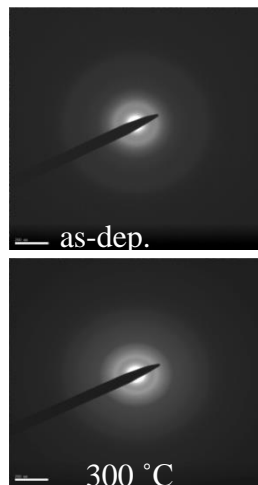
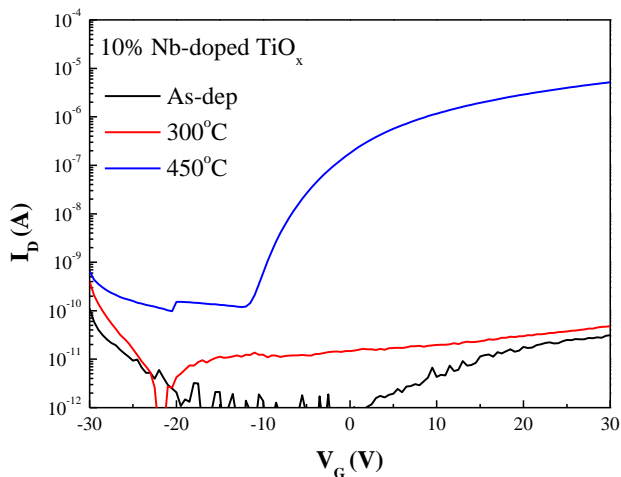


→ Relation between device reliability and band edge state

Chung et. al. JVST-B 31, 021204 (2013)



# Nb or Ta-doped $\text{TiO}_{2-x}$ TFT

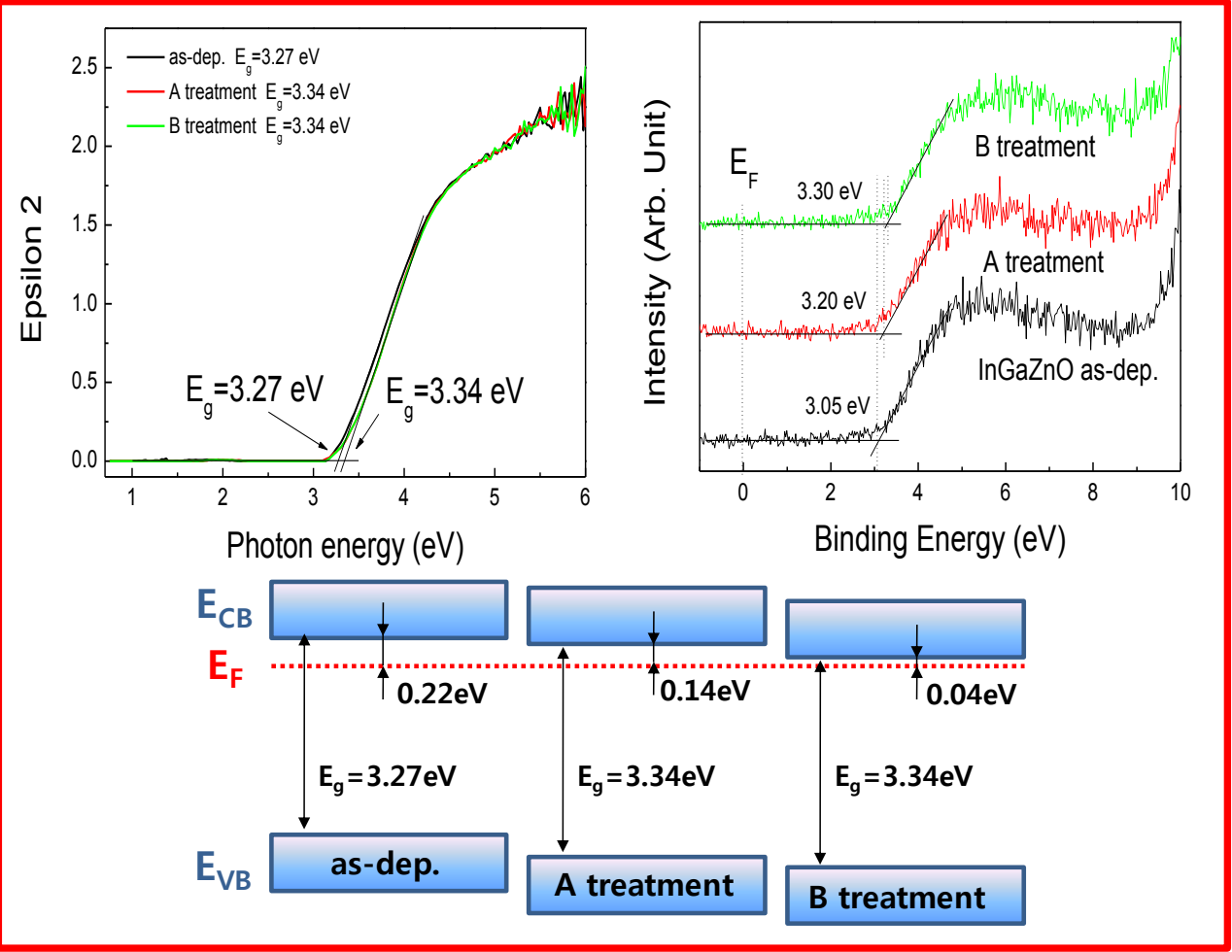
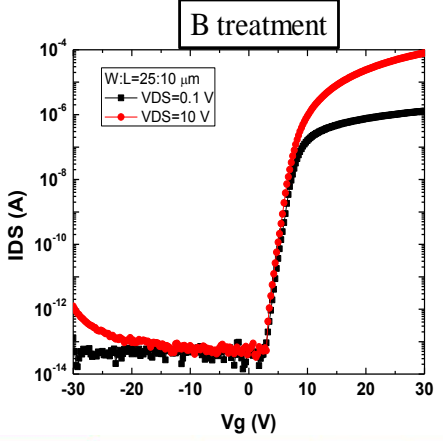
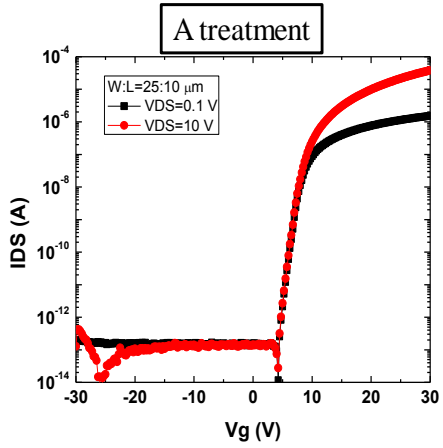
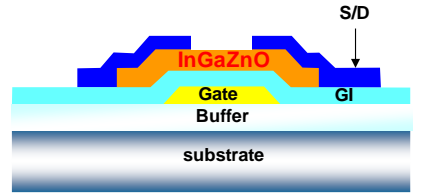


→ Device operation at lower temperature (300°C)

→ Ordering of MO structure of Ta doped  $\text{TiO}_{2-x}$

Chung et. al. APL 103, 213501 (2013)

# InGaZnO TFT

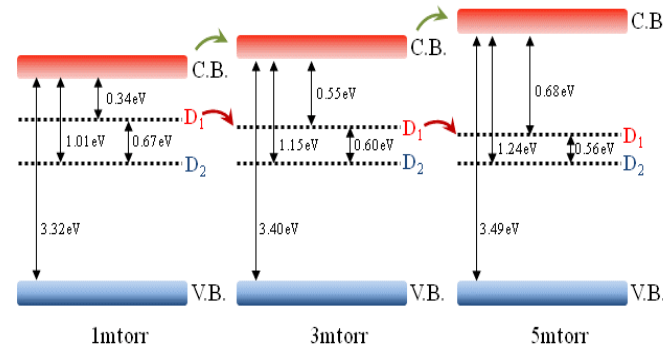
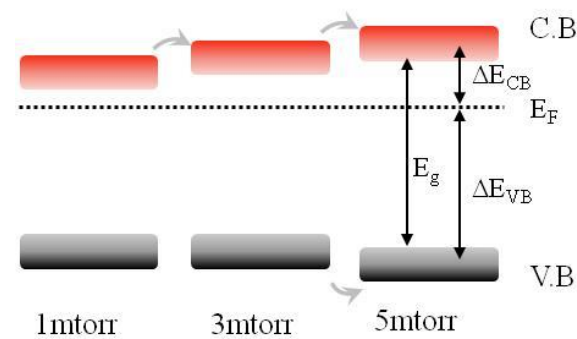
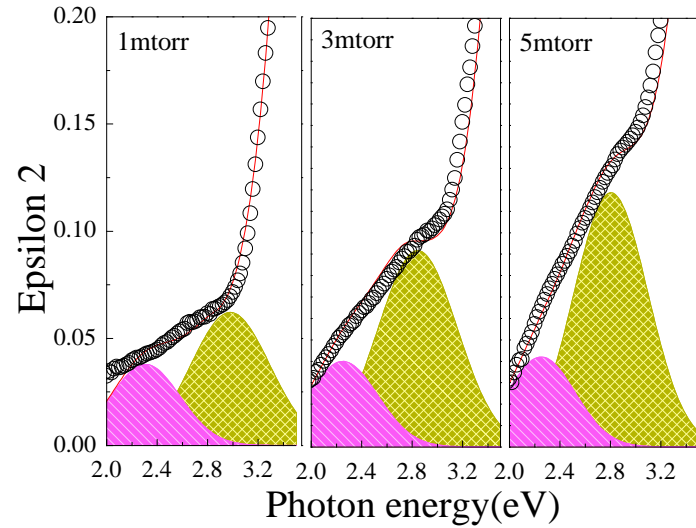
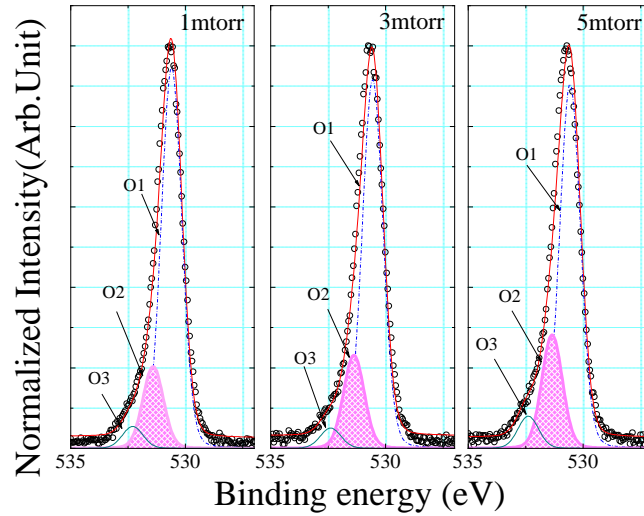
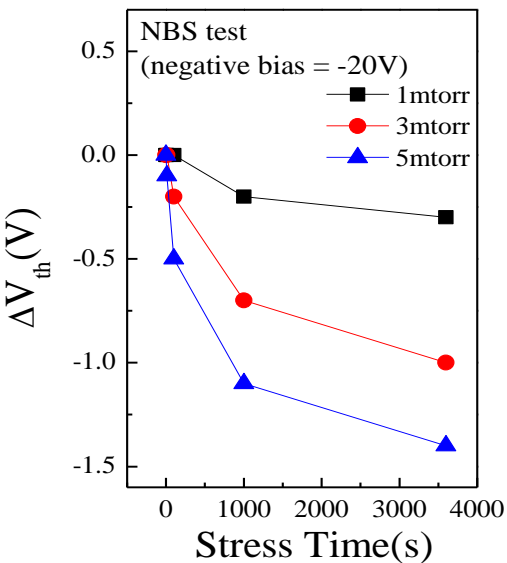
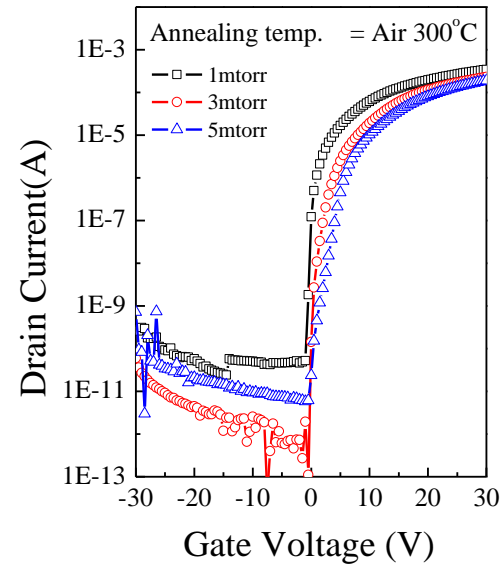


**→ Relation between device performance and band alignment**

Single	Ion	Ioff	Mobility	Vth@1nA	S.S
A treatment	4.06E-05	8.05E-14	6.65	6.00	0.71
B treatment	8.09E-05	1.39E-14	11.32	5.00	

Chung et. al. JPD 45, 415307 (2012)

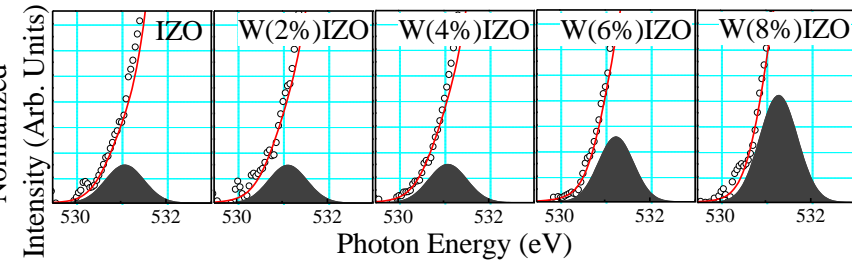
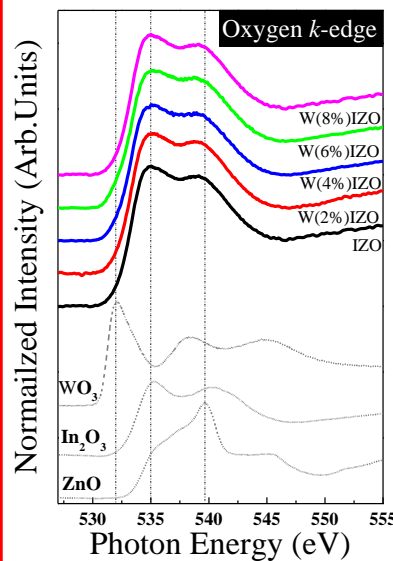
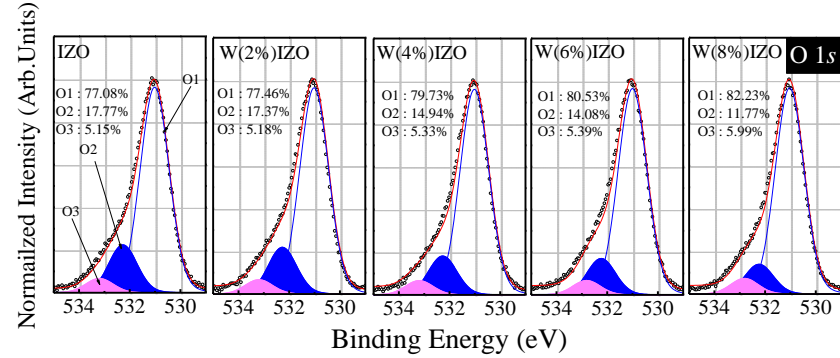
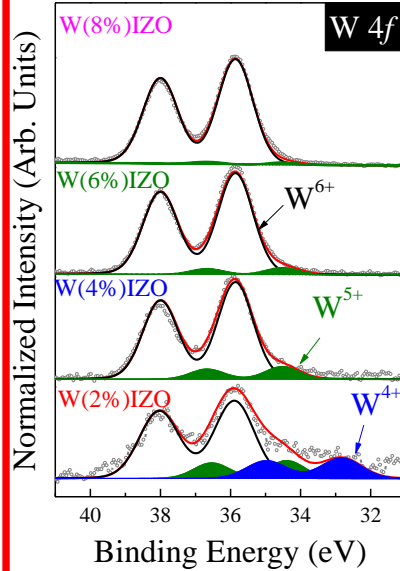
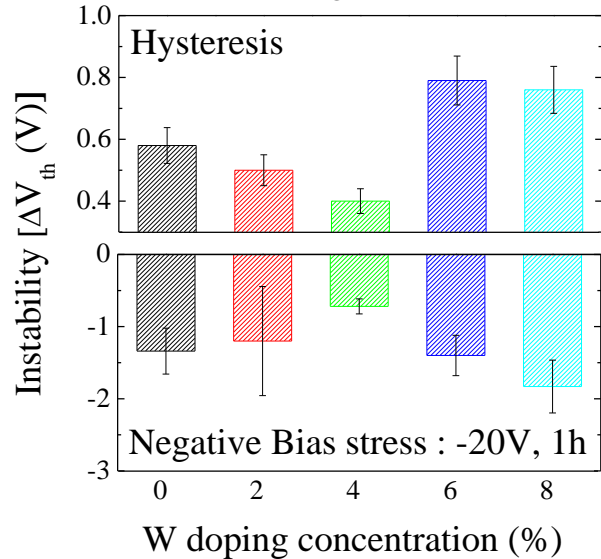
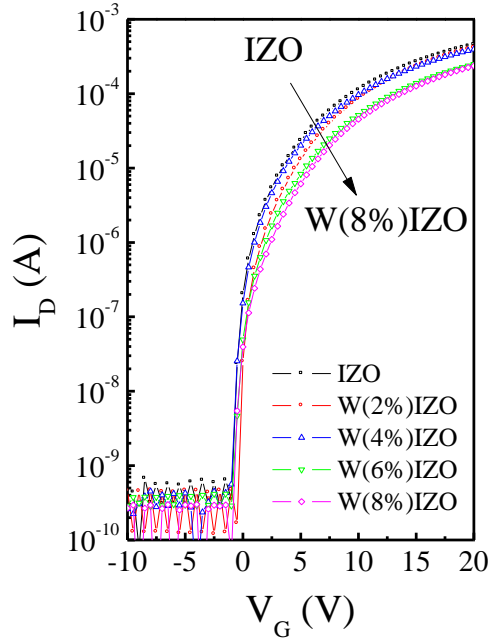
# TaNZnO TFT (Process pressure)



**→ Relation between device performance, stability and vacancies, band edge states, band alignment**

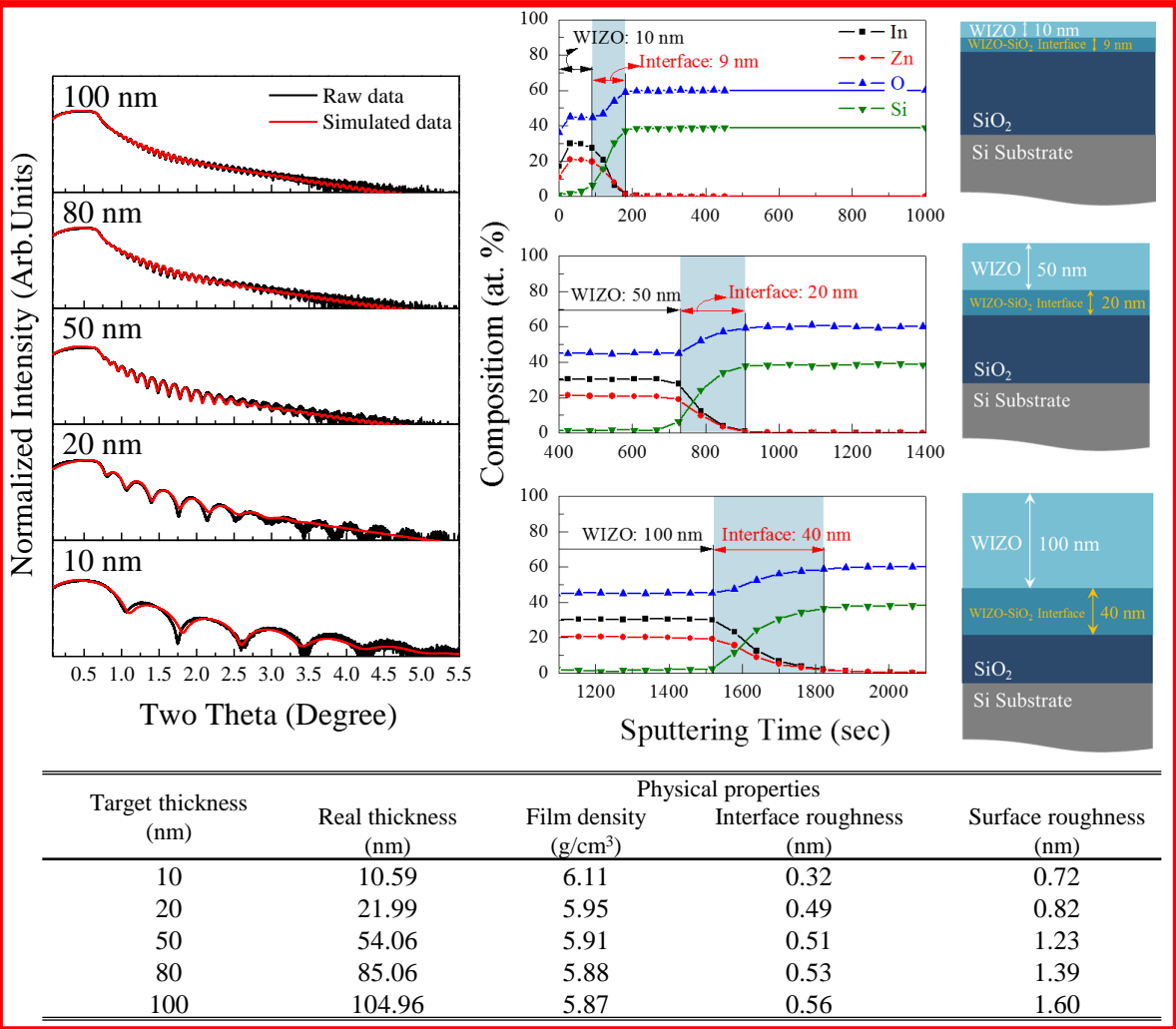
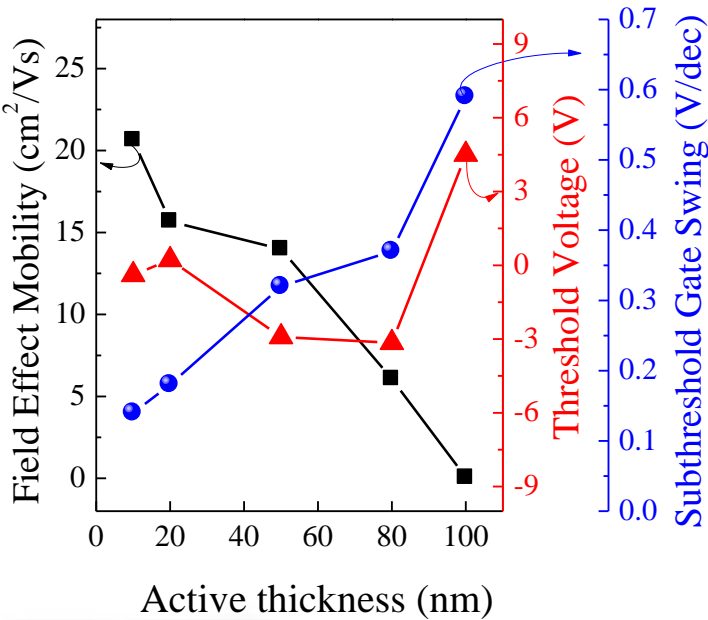
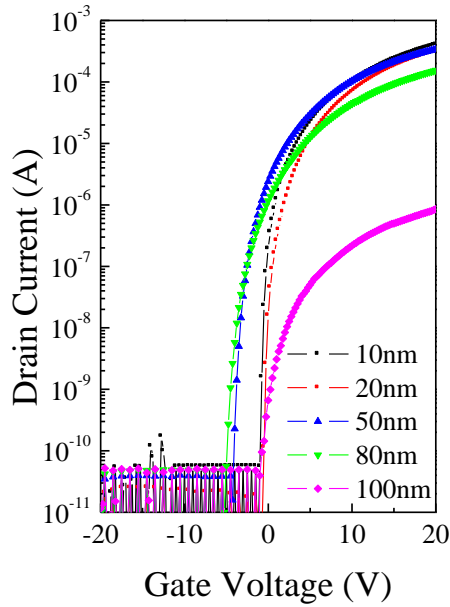
Chung et.al. APL 102, 102102 (2013)

# WInZnO TFT (W concentration)



**→ The role of carrier controller is important for improving the stability of the oxide TFTs**

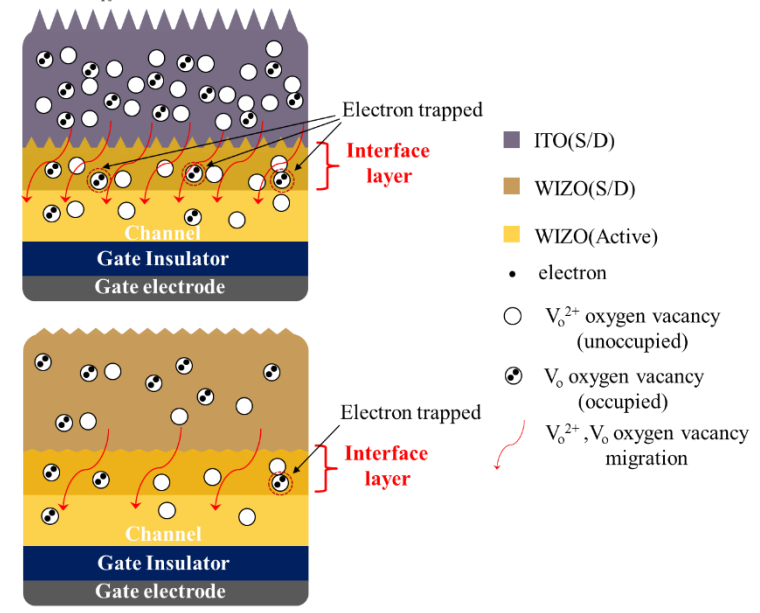
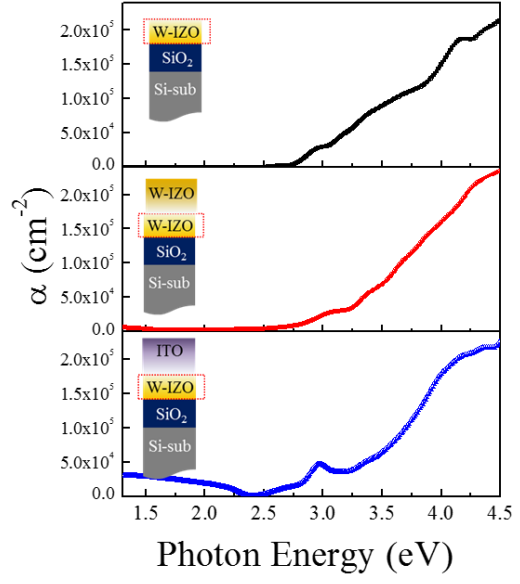
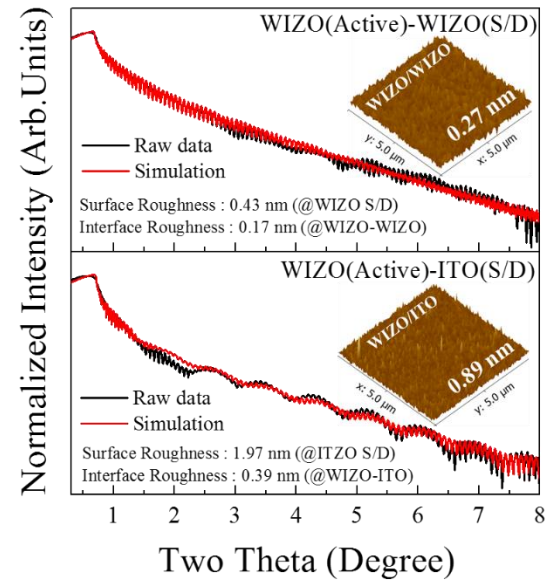
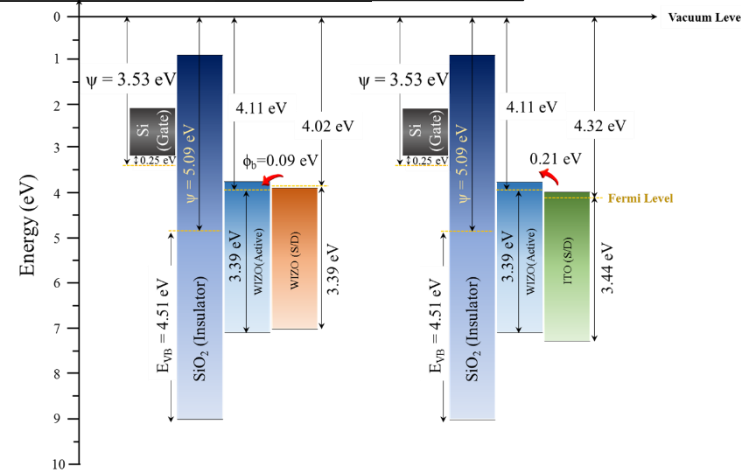
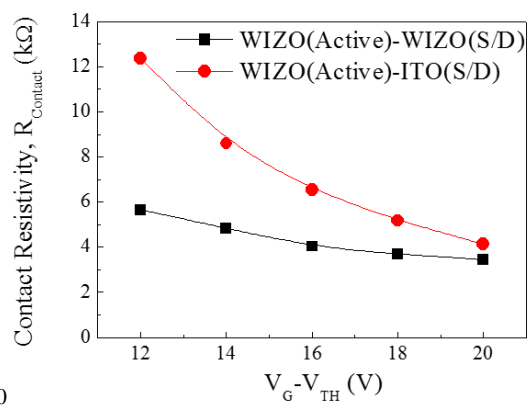
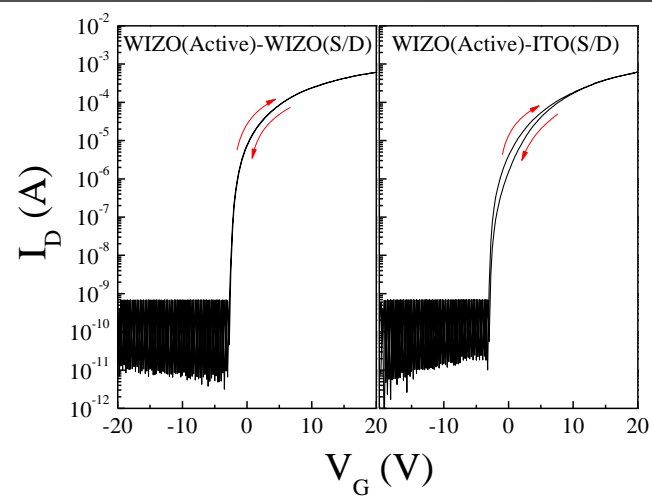
# WInZnO TFT (active layer thickness)



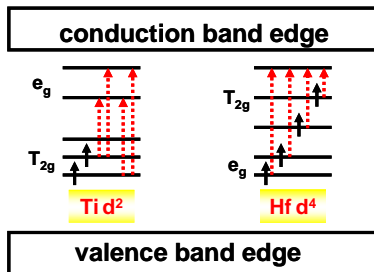
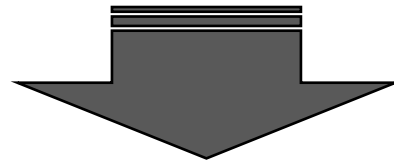
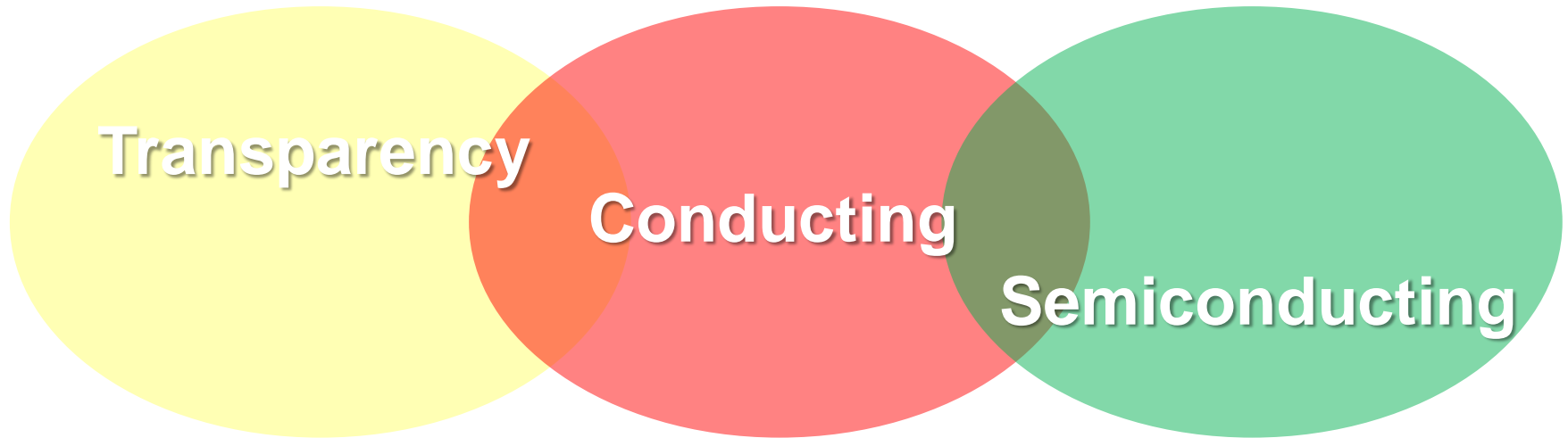
**→ Relation between device performance and film density, interface roughness, interface trap density**



# WInZnO TFT (Homojunction structure)



**→ Homojunction structure is a method that can improve the device performance and Stability**



## ***Analysis of Electronic Structure***

***→ Conduction band, Band edge states, Band alignment, Binding states, Molecular orbital ordering***

**Thank you for your attention!!**

